Compositional Abstraction in Real-Time Model Checking*

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Abstract. The idea to use simulations (or refinements) as a compositional abstraction device is well-known, both in untimed and timed settings, and has already been studied theoretically and practically in many papers during the last three decades. Nevertheless, existing approaches do not handle two fundamental modeling concepts which, for instance, are frequently used in the popular UPPAAL model checker: (1) a parallel composition operator that supports communication via shared variables as well as synchronization of actions, and (2) committed locations. We describe a framework for compositional abstraction based on simulation relations that does support both concepts, and that is suitable for UPPAAL. Our approach is very general and the only essential restriction is that the guards of input transitions do not depend on external variables. We have applied our compositional framework to verify the Zeroconf protocol for an arbitrary number of hosts.

1 Introduction

In this article, we describe a framework for compositional abstraction based on simulation relations that is suitable for the popular model checker UPPAAL [4]. The idea to use simulations (or refinements) as a compositional abstraction device is well-known, both in untimed and timed settings, and has already been studied theoretically and practically in many articles during the last three decades, see for instance [26,24,20,1,25,18,19,15,13,21]. Nevertheless, when we attempted to apply these existing approaches to fight state space explosions in a model of an industrial protocol [14], we ran into the problem that they do not handle two fundamental modeling concepts that are frequently used in UPPAAL.

The first concept is an (asynchronous) parallel composition operator that supports communication via both shared variables and synchronization of actions. Models for reactive systems typically either support communication via shared variables (TLA [22], Reactive Modules [3], etc), or communication via synchronization of actions (CCS [27], I/O automata [24], etc). We are only aware of four studies of compositionality in which the two types of communication are combined [23,19,16,12]. It is well known that both types of communication can be defined in terms of each other. A shared variable can be modeled as a separate

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process/automaton that communicates with its environment via read/write synchronization actions. However, in this approach the evaluation of, for instance, an integer expression may involve a sequence of interactions with shared variable automata. This blows up the state space and makes it more difficult to understand the model. Conversely, synchronization of actions can be modeled in a shared variable setting using some auxiliary flag variables and handshake transitions of the synchronizing automata. But again this blows up the state space and makes it harder to understand the model. The UPPAAL model checker supports both shared variables and synchronization of actions, and this feature is extremely helpful for building tractable models of complex systems.

When combining shared variables and synchronization of actions, one has to deal with the scenario, illustrated in Figure 1, in which the transitions involved in a synchronization assign different values to a shared variable. One simple (but restrictive) approach, pursued by Lynch et al [23], is to impose syntactic conditions which ensure that the scenario does not occur: for each shared variable only one automaton gets write permission, and the other automata may read the variable but not assign a new value to it. A slightly more general approach is taken by Jensen et al [19], where the variables of each automaton are classified as readable and/or writable. Two automata may share writable variables, but in this case a synchronizing transition may only occur if both automata assign the same values to these variables. In practice, this means that multi-writer variables can only be updated via internal (non-synchronizing) transitions. As we describe in [6], the approach of [19] is flawed since parallel composition is not associative; as a result a connection with the standard UPPAAL semantics cannot be established. In the framework of Sociable Interfaces of De Alfaro et al [12], the output transition selects the next value of the global variables, and the input transition is used only to determine the next value of the local variables. The transition relation associated with the output action must respect the constraints specified by the transition relation associated with the input action. In the example of Figure 1 this is only the case when \( v = -1 \) before the synchronization. As we point out in [7], also the parallel composition of [12] is not associative (disproving Theorem 4) in the sense that there exist modules \( M_1, M_2 \) and \( M_3 \) such that \( M_1 \otimes (M_2 \otimes M_3) \) is defined but \( (M_1 \otimes M_2) \otimes M_3 \) is not. A general, process algebraic approach is presented by Groote & Ponse [16]. In this elegant approach one can basically define the desired effect of a synchronization for any pair of actions \( c! \) and \( c? \). However, due to the linking of action names to effects on the global state space, the behavioral equivalence proposed in [16] is extremely fine and not suited as a compositional abstraction device: two configurations can only be related if they assign identical values to all variables.
In this article, we present a very general approach that is consistent with actual the treatment of synchronization in UPPAAL, and that supports compositional abstraction. Unlike [23,19,16,12], UPPAAL deals with the situation of Figure 1 by first performing the assignment on the output transition $c!$, followed by the assignment on the input transition $c?$. This means that after occurrence of the synchronization transition $e$ will have the value 3. Following UPPAAL, we describe synchronization of automata by a rule of the form

$$\frac{r \xrightarrow{c} r' \quad s[r'] \xrightarrow{e} s'}{r \parallel s \xrightarrow{e} r'[s'] || s'}$$

(1)

Here $s[r']$ denotes state $s$ but with the shared variables updated according to $r'$. In UPPAAL, a synchronization may only occur if the guards of both transitions hold, and only if this is the case the assignments are carried out. This means that if we add a guard $v \neq 1$ to the rightmost transition in Figure 1, synchronization will be possible starting from any state satisfying this predicate. In a semantics with rule (1), however, synchronization will no longer be possible since after the assignment on the output transitions has been performed, the guard of the input transition no longer holds. In order to rule out this scenario (which we have never observed in practical applications), our approach imposes the restriction that guards of input transitions do not refer to external (shared) variables. Guards of input transitions may depend on the internal variables, so in general our automata are certainly not input enabled.

The second modeling concept, which is not handled by any existing framework but needed for industrial applications, is the notion of a committed location. In UPPAAL, locations of a timed automata can be designated as committed. If one automaton in a network of automata is in a committed location, time may not progress and the next transition (if any) has to start from a committed location. Committedness is useful to specify that certain transitions need to be executed atomically without intervening transitions from other components. Also, excluding certain behavior with committed locations may lead to serious reductions in the state space of a model [8]. In this article, we present a compositional semantics for committedness. This is achieved by distinguishing, at the semantic level, between committed and uncommitted transitions. Our rules for describing committed locations involve negative antecedents and are similar to the rules that have been proposed in the process algebra literature to describe priorities [11,28,2]: a component may only perform a uncommitted $\tau$-transition if other components may not perform a committed transition. Although there are some subtle differences at the technical level, basically our results show that one may view committedness as a form of priority.

We define the semantics of timed automata in terms of timed transition systems (TTSs). These are labeled transition systems (LTSs) equipped with a bit of additional structure to capture relevant information about state variables, committedness of transitions, and real-time behavior. On TTSs we define the operations of parallel composition and a CCS style restriction operator. An important sanity check for our definitions is that, for any network of timed au
tomata, the (noncompositional) UPPAAL semantics (as defined in the UPPAAL 4.0 help menu) is isomorphic to the (compositional) semantics obtained by associating TTSs to the timed automata in the network, composing these TTSs in parallel, applying a restriction operator, and then considering the underlying LTS. That is, if \( \mathcal{N} = \langle A_1, \ldots, A_n \rangle \) is a network of timed automata then

\[
\text{LTS}(\mathcal{N}) \cong \text{LTS}((\text{TTS}(A_1)) \parallel \cdots \parallel \text{TTS}(A_n)) \setminus C.
\]

A key lemma needed to prove this result is associativity of parallel composition. We define an abstraction relation on TTSs in terms of timed step simulations. These induce a behavioral preorder \( \preceq \) that is somewhere in between strong and weak simulation. If \( T_1 \preceq T_2 \), then \( T_2 \) can either mimic the transitions of \( T_1 \) or (in case of an internal transition) do nothing, but it may not add internal transitions that are not present in \( T_1 \). We establish that \( T_1 \preceq T_2 \) implies \( T_1 \parallel T_3 \preceq T_2 \parallel T_3 \). We briefly summarize the use of our compositional framework in the verification of the Zeroconf protocol for an arbitrary number of hosts [5]. Without our techniques, UPPAAL can only verify instances with three hosts.

Section 2 introduces timed transition systems. In Section 3, we define timed step simulations and establish that the induced preorder is compositional. Section 4 presents networks of timed automata and defines their semantics both noncompositionally (as in UPPAAL) and compositionally in terms of TSSs. Also, the consistency of the two semantics is established and we briefly discuss the application of our framework. Finally, Section 5 discusses some extensions and future research. For lack of space, some technical details and all proofs have been deferred to appendices.

2 Timed Transition Systems

In this section, we introduce the semantic model of timed transition systems (TTSs). Basically, a TTS is a labeled transition system equipped with a bit of additional structure to support shared variables and committed transitions: states are defined as valuations of variables, and transitions may be committed, which gives them priority in a parallel composition. TTSs can be placed in parallel and may communicate by means of shared variables and synchronization of actions. Like in CCS [27], two transitions may synchronize when their actions are complementary, leading to an internal transition in the composition.

Just to fix notation, we first recall the definition of a labeled transition system. Since we consider systems that communicate via shared variables, we find it convenient to model states as functions that map state variables to values in their domain. We also introduce a basic vocabulary for overriding and updating of functions that we need to describe shared variable communication. After these preliminaries, we present the definition of a timed transition system, the operations of parallel composition and restriction, and establish some key properties of these operations, in particular associativity of parallel composition. All the proofs of this section have been deferred to Appendix B. Throughout this article, we write \( \mathbb{R}_{\geq 0} \) for the set of nonnegative real numbers, \( \mathbb{N} \) for the set of
natural numbers, and \( B = \{1, 0\} \) for the set of Booleans. We let \( d \) range over \( \mathbb{R}_{\geq 0} \), \( i, j, k, n \) over \( \mathbb{N} \), and \( b, b', \ldots \) over \( B \).

We consider labeled transition systems with several types of state transitions, corresponding to different sets of actions. We assume a set \( C \) of channels and let \( c \) range over \( C \). The set of external actions is defined as \( \mathcal{E} \triangleq \{c! , c? \mid c \in C\} \). Actions of the form \( c! \) are called output actions and actions of the form \( c? \) are called input actions. We let \( e \) range over \( \mathcal{E} \). We assume the existence of a special internal action \( \tau \), and write \( \mathcal{E}_\tau \) for \( \mathcal{E} \cup \{\tau\} \), the set of discrete actions. We let \( \alpha \) range over \( \mathcal{E}_\tau \). Finally, we assume a set of durations or time-passage actions, which in this article are just the nonnegative real numbers in \( \mathbb{R}_{\geq 0} \). We write \( \text{Act} \) for \( \mathcal{E}_\tau \cup \mathbb{R}_{\geq 0} \), the set of actions, and let \( a, a', \ldots \) range over \( \text{Act} \).

The following definition is standard, except maybe for our specific choice of a universe of transition labels.

**Definition 1 (LTS).** A labeled transition system (LTS) is a tuple \( L = (S, s^0, \rightarrow) \), where \( S \) is a set of states, \( s^0 \in S \) is the initial state, and \( \rightarrow \subseteq S \times \text{Act} \times S \) is the transition relation. We let \( q, r, s, t \ldots \) range over states, and write \( s \xrightarrow{a} t \) if \( (s, a, t) \in \rightarrow \). We refer to \( s \) as the source of the transition, and to \( t \) as the target.

We say that an \( a \)-transition is enabled in \( s \), notation \( s \xrightarrow{a} \), whenever \( s \xrightarrow{a} t \) for some \( t \). A state \( s \) is reachable if there exist states \( s_1, \ldots, s_n \) such that \( s_1 = s^0 \), \( s_n = s \) and, for all \( i < n \) there exists an \( a \) s.t. \( s_i \xrightarrow{a} s_{i+1} \).

We write \( \text{dom}(f) \) to denote the domain of a function \( f \). If also \( X \) is a set, then we write \( f \upharpoonright X \) for the restriction of \( f \) to \( X \), that is, the function \( g \) with \( \text{dom}(g) = \text{dom}(f) \cap X \) such that \( g(z) = f(z) \) for each \( z \in \text{dom}(g) \). For functions \( f \) and \( g \), we let \( f \triangleright g \) denote the left-merge, the combined function where \( f \) overrides \( g \) for all elements in the intersection of their domains.\(^1\) Formally, we define \( f \triangleright g \) to be the function with \( \text{dom}(f \triangleright g) = \text{dom}(f) \cup \text{dom}(g) \) satisfying, for all \( z \in \text{dom}(f \triangleright g) \), \( (f \triangleright g)(z) = f(z) \) if \( z \in \text{dom}(f) \) then \( f(z) \) else \( g(z) \). We define the dual right-merge operator by \( f \triangleleft g \triangleleft g \triangleright f \). Two functions \( f \) and \( g \) are compatible, notation \( f \triangleright g \), if they agree on the intersection of their domains, that is, \( f(z) = g(z) \) for all \( z \in \text{dom}(f) \cap \text{dom}(g) \). For compatible functions \( f \) and \( g \), we define their merge by \( f \parallel g \). Whenever we write \( f \parallel g \), we implicitly assume \( f \triangleright g \). We write \( f \parallel g \) for the update of function \( f \) according to \( g \), that is, \( f \parallel g \triangleq (f \triangleright g) \cap \text{dom}(f) \).

We assume a universal set \( V \) of typed variables, with a subset \( X \subseteq V \) of clock variables or clocks. Clocks have domain \( \mathbb{R}_{\geq 0} \). We let \( y \) range over \( V \) and \( x \) over \( X \). A valuation for a set \( V \subseteq V \) is a function that maps each variable in \( V \) to an element in its domain. We let \( u, v, w, \ldots \) range over valuations, and write \( \text{Val}(V) \) for the set of valuations for \( V \). For valuation \( v \in \text{Val}(V) \) and duration \( d \in \mathbb{R}_{\geq 0} \), we define \( v \oplus d \) to be the valuation for \( V \) that increments clocks by \( d \), and leaves the other variables untouched, that is, for all \( y \in V \),

\[
(v \oplus d)(y) \triangleq \begin{cases} 
\text{if } y \in X \text{ then } v(y) + d \text{ else } v(y) \end{cases}
\]

\(^1\) Essentially, this is the overriding operator “\( \triangleright \)” from Z. On finite domains, the operator is also defined in VDM, where it is written \( \downarrow \). We prefer not to use a symmetric symbol for an asymmetric (non commutative) operator.
A subset \( P \subseteq \text{Val}(V) \) of valuations is called a property over \( V \). Let \( W \supseteq V \) and \( v \in \text{Val}(W) \). We say that \( P \) holds in \( v \), notation \( v \models P \), if \( v[V] \in P \). A property \( P \) over \( V \) is left-closed w.r.t. time-passage if, for all \( v \in \text{Val}(V) \) and \( d \in \mathbb{R}_{\geq 0} \), \( v \oplus d \models P \Rightarrow v \models P \). We say that property \( P \) over \( V \) does not depend on a set of variables \( W \subseteq V \) if, for all \( v \in \text{Val}(V) \) and \( u \in \text{Val}(W) \), \( v \models P \Leftrightarrow v[u] \models P \).

We write \( \{y_1 \mapsto z_1, \ldots, y_n \mapsto z_n\} \) for the valuation that assigns value \( z_i \) to variable \( y_i \), for \( i = 1, \ldots, n \).

The state variables of a TTS are partitioned into external and internal variables. Internal variables may only be updated by the TTS itself and not by its environment. This in contrast to external variables, which may be updated by both the TTS and its environment. A new element in our definition of a TTS is that transitions are classified as either committed or uncommitted. Committed transitions have priority over time-passage transitions and over internal transitions that are not committed. Interestingly, whereas in UPPAAL committedness is an attribute of locations, it must be treated as an attribute of transitions in order to obtain a compositional semantics. This issue is further discussed in Appendix A.

We are now ready to formally define our notion of a timed transition system.

**Definition 2 (TTS).** A timed transition system (TTS) is a tuple

\[
T = (E, H, S, s_0, \alpha \rightarrow^1, \alpha \rightarrow^0),
\]

where \( E, H \subseteq V \) are disjoint sets of external and internal variables, respectively, \( V = E \cup H \), \( S \subseteq \text{Val}(V) \), and \( (S, s_0, \text{Act}, \alpha \rightarrow^1 \cup \alpha \rightarrow^0) \) is an LTS.

We write \( r \xrightarrow{a, b} s \) if \( (r, a, s) \in \alpha \rightarrow^b \). The value \( b \) determines whether or not a transition is committed. We often omit \( b \) when it equals 0. We write \( \text{LTS}(T) \) to denote the underlying LTS of \( T \). We require the following axioms to hold, for all \( s, t \in S \), \( a, a' \in \text{Act} \), \( b \in \mathbb{B} \), \( d \in \mathbb{R}_{\geq 0} \) and \( u \in \text{Val}(E) \),

\[
\begin{align*}
\alpha_1 \tau \rightarrow^a_s & \Rightarrow a' \in E \lor (a' = \tau \land b) \quad \text{(Axiom I)} \\
s[u] & \in S \quad \text{(Axiom II)} \\
s \xrightarrow{c \tau, b} & \Rightarrow s[u] \xrightarrow{c \tau, b} \quad \text{(Axiom III)} \\
s \xrightarrow{d} t & \Rightarrow t = s \oplus d \quad \text{(Axiom IV)}
\end{align*}
\]

A state \( s \) of a TTS is called committed, notation \( \text{Comm}(s) \), iff it enables an outgoing committed transition, that is, \( s \xrightarrow{a, 1} \) for some \( a \). Axiom I states that in a committed state neither time-passage steps nor uncommitted \( \tau \)'s may occur. The axiom implies that committed transitions always have a label in \( E_\tau \). Note that a committed state may have outgoing uncommitted transitions with a label in \( E \).

The reason is that, for instance, an uncommitted \( c \tau \)-transition may synchronize with a committed \( c \tau \)-transition from some other component, and thereby turn into a committed \( \tau \)-transition.

In general, the states of a TTS constitute a proper subset of the set of all valuations of the state variables. This feature is used to model the concept of
location invariants in timed automata: if a timed automaton has, for instance, a location invariant $x \leq 1$ then this automaton may never reach a state in which $x > 1$; semantically speaking states in which $x \leq 1$ do not hold simply do not exist. In a setting with shared variable communication, complications may arise if one component wants to update a shared variable in a way that violates the location invariant of another component. In UPPAAAL, a state transition is only possible if in all location invariants hold in the target state. Our position is that models in which state transitions may violate location invariants are bad models. Therefore, and also because it simplifies the technicalities, we postulate in Axiom II that if the external variables of a state are changed, the result is again a state.

Axiom III states that enabledness of input transitions is not affected by changing the external variables. This is a key property that we need in order to obtain compositionality; we will discuss this axiom in more detail below. Axiom IV, finally, asserts that if time advances with an amount $d$, all clocks also advance with an amount $d$, and the other variables remain unchanged.

We now introduce the operations of parallel composition and restriction on TTSs. In our setting parallel composition is a partial operation that is only defined when TTSs are compatible: the initial states must be compatible functions and the internal variables of one TTS may not intersect with the variables of the other. We can avoid the restriction on the internal variables via a straightforward renaming procedure, but this would complicate the definitions.

From now on, if we have multiple indexed systems (TTSs, or later timed automata), then we use the indices also to denote the components of individual systems. For example, we let $E_i$ denote the set of external variables of $T_i$.

**Definition 3 (Parallel composition).** Two TTSs $T_1$ and $T_2$ are compatible if $H_1 \cap V_2 = H_2 \cap V_1 = \emptyset$ and $s_1 \cup s_2 \neq 0$. In this case, their parallel composition $T_1 \parallel T_2$ is the tuple $T = (E, H, S, s_0, \rightarrow_i, \rightarrow^0)$, where $E = E_1 \cup E_2$, $H = H_1 \cup H_2$, $S = \{ r || s | r \in S_1 \land s \in S_2 \}$, $s_0 = s_1 || s_2$, and $\rightarrow^0$ are the least relations that satisfy the rules in Fig. 2. Here $i, j$ range over $\{1, 2\}$, $r, r'$ range over $S_i$, $s, s'$ range over $S_j$, $b, b'$ range over $B$, $e$ ranges over $E$ and $c$ over $C$.

<table>
<thead>
<tr>
<th>Rule</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>$r \xrightarrow{c, b} r'$</td>
<td>EXT</td>
</tr>
<tr>
<td>$r</td>
<td></td>
</tr>
<tr>
<td>$r \xrightarrow{\pi_{b, i}} r'$</td>
<td>TAU</td>
</tr>
<tr>
<td>$r</td>
<td></td>
</tr>
<tr>
<td>$s \xrightarrow{b}$</td>
<td></td>
</tr>
<tr>
<td>$r</td>
<td></td>
</tr>
<tr>
<td>$r</td>
<td></td>
</tr>
<tr>
<td>$s \xrightarrow{d} s'$</td>
<td></td>
</tr>
<tr>
<td>$b \lor b'$</td>
<td>SYNC</td>
</tr>
</tbody>
</table>

**Fig. 2.** Rules for parallel composition of TTSs

The external and internal variables of the composition are simply obtained by taking the union of the external and internal variables of the components, respectively. The states (and start state) of a composed TTS are obtained by merging the states (resp. start state) of the components (viewed as functions). The interesting part of the definition consists of the transition rules. Rule EXT
states that an external transition of a component induces a corresponding transition of the composition. The component that takes the transition may override some of the shared variables. Observe that, since \( r' \triangleright s = r'||s[r'] \), and since \( s[r'] \) is a state of \( T_3 \) by Axiom II, it follows that \( r' \triangleright s \) is a state of \( T \). Similarly, rule **TAU** states that an internal transition of a component induces a corresponding transition of the composition, except that an uncommitted transition may only occur if the other component is in an uncommitted state. Rule **SYNC** describes the synchronization of components. If \( T_j \) has an output transition from \( r \) to \( r' \), and if \( T_j \) has a corresponding input transition from \( s \), updated by \( r' \), to \( s' \), the composition has a \( \tau \) transition to \( r' \prec s' \). The synchronization is committed if one of the participating transitions is committed. However, an uncommitted synchronization may only occur if both components are in an uncommitted state. By Axiom II for \( T_j \) it follows that in rule **SYNC** \( s[r'] \) is a state of \( T_j \), and by \( r' \prec s' = r'[s'][s'] \) and Axiom II for \( T_j \) it follows that in rule **SYNC** \( r' \prec s' \) is a state of \( T \). Rule **TIME**, finally, states that a time step \( d \) of the composition may occur when both components perform a time step \( d \). Observe that \( r' \triangleleft s \) and Axiom IV for both \( T_1 \) and \( T_2 \) imply \( r' \triangleleft s' \). One may check that composition is a well-defined operation on TTSs.

Commutativity and associativity are highly desirable properties for parallel composition operators. However, associativity becomes very tricky in a setting with both shared variables and synchronization of actions. In [6,7], we have shown that the composition operators defined in two published papers [19,12] is not associative. We claim that the parallel composition operator defined in this article is both commutative and associative. Commutativity is immediate from the symmetry in the definitions. The proof of associativity is more involved and presented in Appendix B. A key step needed in order to make this proof tractable is to first derive a series of basic laws for \( \triangleright, ||, .[.], \) and \( \triangledown \).

**Theorem 1.** Composition of compatible TTSs is commutative and associative.

The next definition introduces a standard restriction operator, very similar to the one in CCS [27]. The restriction operator internalizes a set of channels so that no further TTSs may synchronize on it.

**Definition 4 (Restriction).** Given a TTS \( T \) and a set \( C \subseteq C \) of channels, we define \( T \setminus C \) to be the TTS that is identical to \( T \), except that all transitions with a label in \( \{c!, c? | c \in C\} \) have been removed from the transition relations.

We write \( \Sigma(T) \) for the set of channels that occur in transitions of \( T \). Using this notation, we can formulate restriction laws, as in CCS [27][p80], such as \( (T || T') \setminus C = T || (T' \setminus C) \) if \( \Sigma(T) \cap C = \emptyset \)

### 3 Compositional Abstraction

In our approach, timed step simulations capture what it means that one TTS is an abstraction of another. In this section, we formally define timed step simulations and establish compositionality of the induced preorder.
A timed step simulation relates the states of two TTSs that have the same external interface, that is, the same sets of external variables. Initial states must always be related. Also, related states must agree on their external variables, and the relation must be preserved by consistently changing the external variables. If the low level system does a step, then either this can be simulated by an identical step in the high level system that preserves the relation, or the low level step is an internal computation step that preserves the simulation relation. Finally, high level committed states may only be related to low level committed states.

Definition 5 (Timed step simulation). Two TTSs $T_1$ and $T_2$ are comparable if they have the same external variables, that is $E_1 = E_2$. Given comparable TTSs $T_1$ and $T_2$, we say that a relation $R \subseteq S_1 \times S_2$ is a timed step simulation from $T_1$ to $T_2$, provided that $s_1^0 R s_2^0$ and if $s R r$ then

1. $\forall E_1 : s^1 R s_2^1$,
2. $\forall u \in Val(E_1) : s^1[u] R s_2^1[u]$,
3. if $\text{Comm}(r)$ then $\text{Comm}(s)$,
4. if $s \xrightarrow{a,b} s'$ then either there exists an $r'$ such that $r \xrightarrow{a,b} r'$ and $s' R r'$, or $a = \tau$ and $s' R r$.

We write $T_1 \leq T_2$ when there exists a timed step simulation from $T_1$ to $T_2$.

It is straightforward to prove that $\leq$ is a preorder on the class of TTS, that is, $\leq$ is reflexive and transitive. Our first main theorem, proved in Appendix C, states that $\leq$ is a precongruence for parallel composition. This means that timed step simulations can be used as a compositional abstraction device.

Theorem 2. Let $T_1, T_2, T_3$ be TTSs with $T_1$ and $T_2$ comparable, $T_1 \leq T_2$, and both $T_1$ and $T_2$ compatible with $T_3$. Then $T_1 \parallel T_3 \leq T_2 \parallel T_3$.

The timed step simulation preorder $\leq$ is in general not a precongruence for restriction. The problem is that the restriction operator removes transitions: this may affect enabledness of committed transitions and invalidate the property that high-level committed states may only be related to low-level committed states. In the theorem below, we explicitly add the condition needed for compositionality: if a state is committed in $T_1$ it should still be committed in $T_1 \backslash C$.

Theorem 3. Let $T_1$ and $T_2$ be comparable TTSs such that $T_1 \leq T_2$. Let $C \subseteq C$. If, for all states $s$ of $T_1$, $\text{Comm}(s) \Rightarrow \exists a \in E_\tau - \{c\!, c?' | c \in C\} : s \xrightarrow{a,1} \text{then } T_1 \backslash C \leq T_2 \backslash C$.

In practice, the side condition of Theorem 3 is unproblematic, for instance because in committed locations of components in a network only output transitions are enabled, and the corresponding input transitions are always enabled in other components. In such a network, a committed state always enables a committed $\tau$-transition, which implies the side condition.
4 Networks of Timed Automata

In this section, we introduce networks of timed automata (NTA), a mathematical model for describing real-time systems inspired by the UPPAAL modeling language. We present two different definitions of the semantics of NTAs and establish their equivalence. The first definition is not compositional and closely follows the UPPAAL semantics (as defined in the UPPAAL 4.0 help menu). The second definition constructs an LTS compositionally by first associating a TTS to each TA in the network, composing these, applying a restriction operator, and then considering the underlying LTS.

An NTA consists of a number of timed automata that may communicate via synchronization of transition labels and via a global set of multi-reader/multi-writer variables. Our model supports committed locations and a restricted form of urgency by allowing internal transitions to be urgent. Our definition of timed automata abstracts from syntactic details and the various restrictions from UPPAAL that are needed to make model checking decidable. These aspects that are not relevant for our compositionality result. However, in order to obtain compositionality, we need to impose some axioms on timed automata that are not required by UPPAAL. Also, several UPPAAL features have not been incorporated within our NTA model, in particular broadcast channels, urgent synchronization channels, and priorities. We expect that these features can be incorporated in our approach (at the price of complicating the definitions) but it remains future work to work out the details.

Definition 6 (TA). A timed automaton (TA) is defined to be a tuple \( \mathcal{A} = (L, K, l^0, E, H, v_0, I, \longrightarrow, \rightarrow^u) \), where \( L \) is a set of locations, \( K \subseteq L \) is a set of committed locations, \( l^0 \in L \) is the initial location, \( E, H \subseteq V \) are disjoint sets of external and hidden variables, respectively, \( V = E \cup H \), \( v^0 \in Val(V) \) is the initial valuation, \( I : L \rightarrow 2^{Val(V)} \) assigns a left-closed invariant property to each location such that \( v^0 \models I(l^0) \),

\[
\longrightarrow \subseteq L \times 2^{Val(V)} \times \mathcal{E} \times (Val(V) \rightarrow Val(V)) \times L
\]

is the transition relation, and \( \rightarrow^u \subseteq \longrightarrow \) is the urgent transition relation. We let \( l, \ldots \) range over locations, write \( l \xrightarrow{g,\alpha,\rho} l' \) if \( (l, g, \alpha, \rho, l') \in \longrightarrow \), refer to \( l \) as the source of the transition, to \( l' \) as the target, to \( g \) as the guard, and to \( \rho \) as the update (or reset) function. We require:

\( I(l) \) does not depend on \( E \) \hspace{1cm} (Axiom V)

\( l \xrightarrow{g,\alpha,\rho} l' \Rightarrow g \) does not depend on \( E \) \hspace{1cm} (Axiom VI)

\( \forall l \in K \forall v \in I(l) \exists (l \xrightarrow{g,\alpha,\rho} l') : v \models g \land \rho(v) \models I(l') \) \hspace{1cm} (Axiom VII)

\( l \xrightarrow{g,\alpha,\rho} l' \Rightarrow \alpha = \tau \land g \) does not depend on \( \mathcal{X} \) \hspace{1cm} (Axiom VIII)

\(^2\) Urgent internal transitions can be encoded in UPPAAL by declaring a special urgent broadcast channel urg, labeling urgent internal transitions by urg!, and ensuring that no transitions carry the label urg?. Urgent internal transitions are very convenient for modeling systems since they allow one to specify that a component reacts instantaneously to some change of the external variables.
Recall that a property \( P \) is left-closed if, for all \( v \in \text{Val}(V) \) and \( d \in \mathbb{R}_{>0} \), \( v \oplus d \models P \Rightarrow v \models P \). In UPPAAL, left-closedness of location invariants is ensured syntactically by disallowing lower bounds on clocks in invariants. Axiom V asserts that location invariants do not depend on external variables. This restriction is not imposed by UPPAAL, but run-time errors may occur in UPPAAL when one automaton modifies external variables in a way that violates the location invariant of another automaton. Although it may be possible to come up with a compositional semantics for a setting without Axiom V, it is clear that the axiom eliminates a number of technical complications. We are not aware of UPPAAL applications in which the axiom is violated. Axiom VI asserts that the guards of input transitions do not depend on external variables. This is a key axiom that we need for our approach to work: it ensures that the update function of an output transition does not affect the enablesness of matching input transitions. Axiom VII states that in a committed location always at least one transition is possible. We need this axiom to ensure that a state in the TTS semantics of a timed automaton is committed iff the corresponding location is committed. The axiom is a prerequisite for what is called time reactivity in [29] and timelock freedom in [9], that is, whenever time progress stops there exists at least one enabled transition. UPPAAL does not impose this axiom, but we would like to argue that any model that does not satisfy it is a bad model. Axiom VIII, finally, states that only internal transitions can be urgent and that the guards of urgent transitions may not depend on clocks. The constraint that urgent transitions may not depend on clocks is syntactically enforced in UPPAAL by requiring that clock variables may not occur in the guards of urgent transitions.

A network of timed automata can now be defined as a finite collection of compatible timed automata:

**Definition 7 (NTA).** Two timed automata \( A_1 \) and \( A_2 \) are compatible if \( H_1 \cap V_2 = H_2 \cap V_1 = \emptyset \) and \( v^0_1 \ominus v^0_2 \). A network of timed automata (NTA) consists of a finite collection \( \mathcal{N} = (A_1, \ldots, A_n) \) of pairwise compatible timed automata.

The operational semantics of NTAs can be defined in terms of labeled transition systems.

**Definition 8 (LTS semantics of NTA).** Let \( \mathcal{N} = (A_1, \ldots, A_n) \) be an NTA. Let \( V = \bigcup_i (V_i \cup \{\text{loc}_i\}) \), with for each \( i \), \( \text{loc}_i \) a fresh variable with type \( I_i \). The semantics of \( \mathcal{N} \), notation \( \text{LTS}(\mathcal{N}) \), is the LTS \( \langle S, s^0, \longrightarrow \rangle \), where

\[
S = \{v \in \text{Val}(V) \mid \forall i : v \models I_i(v(\text{loc}_i))\},
\]

\[
s^0 = v^0_1 \parallel \cdots \parallel v^0_n \parallel \{\text{loc}_1 \mapsto l^0_1, \ldots, \text{loc}_n \mapsto l^0_n\},
\]

and \( \longrightarrow \) is defined by the rules in Fig. 3. We use the convention that if an update function \( \rho : \text{Val}(W) \to \text{Val}(W) \) is applied to a valuation \( v \in \text{Val}(W') \) with \( W \subset W' \), it only affects the variables in \( W \), that is \( \rho(v) \triangleq v[\rho(v[W])]. \)

Definition 8 describes the semantics of an NTA in terms of an LTS. The states of this LTS are valuations of a set \( V \) of variables. This set \( V \) contains the variables
of all TAs and also, for each TA $A_i$, a special variable $\text{loc}_i$ to store the current location of $A_i$. The set of states $S$ only contains valuations in which the location invariants for all TAs hold. The initial state $s^0$ is the state where all automata are in their initial location and all variables have their initial value.

The transition relation $\rightarrow$ contains two kinds of transitions: delay transitions and action transitions. We have a delay transition $s \xrightarrow{d} s'$ if $s$ contains no committed locations, no urgent transition is enabled in $s$, and $s'$ is obtained from $s$ by incrementing all clocks with $d$ and leaving the other variables unchanged, that is $s' = s \oplus d$. Note that, since $s'$ is a state, $s'$ satisfies the location invariants. In fact, since we require that location invariants are left-closed, we have that, for all $d' \in [0, d]$, $s \oplus d'$ satisfies the location invariants. Also, since the guards of urgent transitions may not depend on clocks, we have that, for all $d' \in [0, d]$, $s \oplus d'$ does not enable any urgent transition.

For action transitions there are two cases: internal transitions and binary synchronizations. We have an internal transition $s \xrightarrow{\tau} s'$ if there is an automaton $A_i$ that enables an internal transition $l \xrightarrow{\text{g,\,c,\,p}_i} l'$: $s(\text{loc}_i) = l$ and $s \models g$. We have a synchronization transition $s \xrightarrow{s} s'$ if there are distinct components $A_i$ and $A_j$ that enable an output transition $l_i \xrightarrow{g_i,\text{c},\text{p}_i} l'_i$ and input transition $l_j \xrightarrow{g_j,\text{c},\text{p}_j} l'_j$, respectively. We require that either $l_i$ or $l_j$ is committed, or no location in $s$ is committed. State $s'$ is obtained from $s$ by first applying update $\rho_i$ and then update $\rho_j$. In addition the location variables are updated.

---

**Fig. 3.** UPFAAL style LTS semantics of an NTA
The key step towards a compositional semantics of NTAs is the definition below, which associates a TTS to an individual TA. Essentially this is a simplified version of Definition 8 in which a transition is made committed iff it originates from a committed location.

**Definition 9 (TTS semantics of TA).** Let \( A = (L, K, l^0, E, H, v^0, I, \rightarrow) \) be a TA. The TTS associated to \( A \), notation \( TTS(A) \), is the tuple
\[
(E, H \cup \{loc\}, S, s^0, \rightarrow^1, \rightarrow^0),
\]
where \( loc \) is a fresh variable with type \( L \), \( W = E \cup H \cup \{loc\}, S = \{v \in Val(W) \mid v \models I(v(loc))\}, s^0 = v^0 \parallel \{loc \rightarrow l^0\} \), and the transitions are defined by the rules in Fig. 4.

We can check that the structure that we have just defined is indeed a TTS. We now come to our second main theorem, which states that a compositional semantics of NTAs defined in terms of TTSs coincides (modulo isomorphism) with the noncompositional UPPAAL style semantics of Definition 8.

**Theorem 4.** Let \( N = (A_1, \ldots, A_n) \) be an NTA. Then
\[
LTS(N) \cong LTS((TTS(A_1))\parallel \cdots \parallel TTS(A_n))\parallel \mathcal{C}).
\]

In the remainder of this section, we briefly discuss how the previous results may help to alleviate the state space explosion problem. Simulation preorders preserve a rich class of properties (for instance, for Kripke structures all \( \forall \text{CTL}^* \) properties, see [17]), but for simplicity we limit ourselves here to verification of invariants.

**Definition 10 (Invariants).** Let \( L = (S, s^0, \rightarrow) \) be an LTS with \( S \subseteq Val(V) \), for some set of variables \( V \). Let \( P \) be a property over a subset of the variables of \( V \). We say that \( P \) is an invariant of \( L \), notation \( L \models \forall \square P \), iff, for all reachable states \( s \) of \( L \), \( s \models P \).

By extension, we say that \( P \) is an invariant of an NTA \( N \), notation \( N \models \forall \square P \), iff it is an invariant of \( LTS(N) \), and that \( P \) is an invariant of a TTS \( T \), notation \( T \models \forall \square P \), iff it is an invariant of \( LTS(T) \).
Timed step simulations preserve invariant properties in one direction: if an invariant property holds for the abstract system, we may conclude it also holds for the concrete system.

**Theorem 5.** Let $T_1$ and $T_2$ be comparable TTSs such that $T_1 \preceq T_2$. Let $P$ be a property over the external variables of $T_1$ and $T_2$. If $T_2 \models \forall \Box P$, then $T_1 \models \forall \Box P$.

Since our compositional semantics is consistent with the UPPAAL semantics, we can apply our abstraction results to networks of UPPAAL automata as follows. With abuse of notation write $A_1 \parallel \cdots \parallel A_i \preceq B_i \parallel \cdots \parallel B_j$ if $LTS(A_1) \cdots LTS(A_i) \preceq LTS(B_1) \cdots LTS(B_j)$. Assume that $A_1 \cdots A_i \preceq B_1 \cdots B_j$, and the timed automata on the right-hand-side are simpler than those on the left-hand-side. Then, by the definitions and straightforward application of Theorems 2, 3 (assuming the side condition holds), 4 and 5,

$$(B_1, \ldots, B_j, A_{i+1}, \ldots, A_n) \models \forall \Box P \quad \Rightarrow \quad (A_1, \ldots, A_n) \models \forall \Box P$$

Thus, instead of model checking $(A_1, \ldots, A_n)$ it suffices to model check the simpler system obtained by substituting $B_1, \ldots, B_j$ for $A_1, \ldots, A_i$. Variations of this result can be obtained by using the restriction laws of CCS.

We have successfully used this approach in order to analyze Zeroconf, a protocol for dynamic configuration of IPv4 link-local addresses defined by the IETF [14,5]. Below we briefly summarize the different types of abstractions that we applied, and which all can be formally justified using timed step simulations:

1. Weakening guards and location invariants of component timed automata. Use of this type of “overapproximations” can be automatically checked using a general purpose theorem prover.
2. After weakening guards and location invariants, state variables that are not mentioned in the global invariant and that are no longer tested in guards, can be omitted. Again such transformations can be automatically checked using a general purpose theorem prover.
3. In order to verify instances of the protocol with an arbitrary number of hosts, we applied the “Spotlight” principle of [30] and abstracted all hosts except two into a “chaos” automaton, a very coarse abstraction with a single state that enables every action at any time. It should be routine to check this transformation formally using a proof assistant.
4. At some point, we abstracted one automaton by a composition of two automata. Unlike the other abstractions, proving correctness of this abstraction by hand turned out to be nontrivial. With help of Thomas Chatain, we succeeded to prove existence of a timed step simulation fully automatically using UPPAAL-TIGA [10], a branch of UPPAAL that is able to solve timed games on NTAs. It turns out that a timed step simulation corresponds to a winning strategy for a certain timed game. By using the abstractions, UPPAAL was able to verify the Zeroconf for an arbitrary number of hosts. Without our techniques, UPPAAL can only handle instances with three hosts.

### 5 Future Work

Our framework deals with an important part of the UPPAAL modeling language, and is for instance suitable for dealing with the Zeroconf protocol. Nevertheless,
several features have not been dealt with, notably:

- **Urgent channels.** Our approach supports urgent internal transitions but not general urgent channels as in UPFAAL. Shared variables in combination with urgent internal transitions are very expressive, though, and we have never felt the need to use urgent synchronization channels in any of the numerous UPFAAL applications that we have been involved in. We expect that urgent channels can be easily incorporated using the concept of timed ready simulations from [18].

- **Broadcast communication.** General broadcast communication, as supported by UPFAAL, does not have a neat semantics: the order in which automata are declared influences the semantics of a network. It should be possible though to identify a well-behaved subset (for instance, by requiring that the variables modified by different input actions be disjoint). Once this has been done, we expect that the results of this paper can easily be generalized.

- **Priorities.** UPFAAL supports channel priorities. As we have shown, committed locations induce a priority mechanism, and we expect that channel priorities can be described in an analogous manner.

Conceptually there are no major difficulties involved in generalizing our results to a setting which includes these features, but the proofs will become tedious and long. Since UPFAAL is extended all the time, we envisage that proof assistants such as Isabelle and PVS will become indispensable for establishing correctness of verification methods.

Although from a theoretical viewpoint implementing our framework may be less interesting, from a practical viewpoint it is all the more. We envisage a version of UPFAAL that maintains networks of timed automata at different levels of abstraction, and which can automatically prove correctness of abstractions using UPFAAL-TIGA and theorem proving technology.

Since we phrased our compositionality results very abstractly in terms of timed transition systems, which may (or may not) have time-passage transitions and may (or may not) have committed transitions, our results can be reused directly in the design of other practical modeling languages with both shared variables and synchronization of actions.

References

A Committed States versus Committed Transitions

In TTSs committedness is an attribute of transitions. This contrasts with the Uppaal syntax, where committedness is defined as an attribute of locations, which are part of the state. So why don’t we follow Uppaal? This would have the additional advantage that the rules for composition can be simplified to those of Figure 5. The problem has to do with the definition of committedness.

<table>
<thead>
<tr>
<th>Rule</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>EXT</td>
<td>$r \xrightarrow{s} r'$, $r \parallel s \xrightarrow{r'} s$</td>
</tr>
<tr>
<td>TAU</td>
<td>$r \xrightarrow{s} r'$, $\text{Comm}(s) \Rightarrow \text{Comm}(r)$, $r \parallel s \xrightarrow{r'} s$</td>
</tr>
<tr>
<td>SYNC</td>
<td>$r \xrightarrow{s} r'$, $s[r'] \xrightarrow{t} s'$, $i \neq j$, $r \parallel s \xrightarrow{r'} s'$</td>
</tr>
<tr>
<td>TIME</td>
<td>$r \xrightarrow{s} r'$, $s \xrightarrow{d_j} s'$, $i \neq j$, $r \parallel s \xrightarrow{d_j} s'$</td>
</tr>
</tbody>
</table>

**Fig. 5.** Oversimplified rules for parallel composition of TTSs

for composed states. There appears to be a choice between defining $r \parallel s$ to be committed if $r$ and $s$ are committed, or defining $r \parallel s$ to be committed if $r$ or $s$ is committed. In order to see that both choices are wrong, consider the four TTSs in Fig. 6, each consisting of only two states, where a $\subset$ inside a state indicates that is is committed. In the conjunctive scenario, there is no transition $((q\parallel r)\parallel s)\parallel t \xrightarrow{r'} ((q\parallel r')\parallel s)\parallel t$, even though this is allowed according to Uppaal. In the disjunctive scenario, there is a transition $((q\parallel r)\parallel s)\parallel t \xrightarrow{r'} ((q'\parallel r)\parallel s')\parallel t$, which is not allowed according to Uppaal.

**Fig. 6.** A problem with committed states

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B Proofs from Section 2

The following basic properties of $\triangleright$, $\parallel$, $[,]$ and $\triangledown$ are used frequently in proofs. Surprisingly, even though $\triangleright$, $\parallel$ and $[,]$ are (arguably) equally fundamental as function composition, we have not been able to find proofs of these properties in the literature.

Lemma 1. For all functions $f$, $g$ and $h$,

\begin{align*}
(f \triangleright g) \triangleright h &= f \triangleright (g \triangleright h) \quad (2) \\
f \triangledown g &\Leftarrow g \triangledown f \quad (3) \\
f \parallel g &= g \parallel f \quad (4) \\
f \triangledown g \land (f \parallel g) \triangledown h &\Leftarrow f \triangledown g \land f \triangledown h \land g \triangledown h \quad (5) \\
f \parallel (g \triangledown h) &= (f \parallel g) \triangledown h \quad (6) \\
f \triangledown g &\Leftarrow f = f \parallel g \quad (7) \\
f \triangledown g[f] &= f \triangledown g[f] \quad (8) \\
f \triangleright g &= f \parallel [g[f]] \quad (9) \\
f \triangledown g[f] &= f \triangledown g[h] \quad (10) \\
(f \triangleright g)[h] &= f[h] \triangleright (g[h]) \quad (11) \\
f[g][h] &= f[h] \triangleright g \quad (12)
\end{align*}

Lemma 2. Let $T_1$ and $T_2$ be compatible TTSs. Let $r \in S_1$ and $s \in S_2$ such that $r \triangledown s$. Then $\text{Comm}(r\parallel s) \Leftrightarrow \text{Comm}(r) \lor \text{Comm}(s)$.

Proof. \Rightarrow Suppose that $\text{Comm}(r\parallel s)$. Then there exists a transition of the form $r\parallel s \xrightarrow{a,1} s'$. Since the transition is committed, it can only be established using rules EXT, TAU or SYNC. Clearly, if it is established using rule EXT or TAU with $i = 1$ then $\text{Comm}(r)$. Similarly, if it is established using rule EXT or TAU with $i = 2$ then $\text{Comm}(s)$. In both cases $\text{Comm}(r) \lor \text{Comm}(s)$, as required. Now suppose that the transition $r\parallel s \xrightarrow{a,1} s'$ is established using rule SYNC. Assume w.l.o.g. that $i = 1$. Then there are transitions $r \xrightarrow{c_1,b} r''$ and $s[r''] \xrightarrow{c_2,b'} s''$ such that $b \lor b'$. By Axiom III, $s \xrightarrow{c_2,b'}$. Hence either $\text{Comm}(r)$ or $\text{Comm}(s)$, as required.

\Leftarrow Now suppose $\text{Comm}(r) \lor \text{Comm}(s)$. W.l.o.g. assume that $\text{Comm}(r)$. Then, using Axiom I, we know that $r \xrightarrow{a,1}$, for some $a \in \mathcal{E}_r$. By application of either rule EXT or rule TAU, this implies $r\parallel s \xrightarrow{a,1}$. Hence $\text{Comm}(r\parallel s)$. \qed

Lemma 3 (Composition well-defined). Let $T_1$ and $T_2$ be compatible TTSs. Then $T_1 \parallel T_2$ is a TTS.

Proof. Since $E_1 \cap H_1 = \emptyset$ and $E_2 \cap H_2 = \emptyset$ ($T_1$ and $T_2$ are TTSs), and $E_1 \cap H_2 = \emptyset$ and $E_2 \cap H_1 = \emptyset$ ($T_1$ and $T_2$ are compatible), $E \cap H = \emptyset$. By definition, $S \subseteq \text{Val}(V)$ and $s^0 \in S$. We check that $T_1 \parallel T_2$ satisfies the four axioms for a TTS. Suppose $r \in S_1$, $s \in S_2$ and $r \triangledown s$. 

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1. Assume that \( r \parallel s \xrightarrow{a_1.b} r' \) and \( r \parallel s \xrightarrow{a'.b} r'' \). In order to prove Axiom I, we must establish that \( a' \in E \lor (a' = \tau \land b) \). By Lemma 2, either \( \text{Comm}(r) \) or \( \text{Comm}(s) \). By Axiom I for \( T_1 \) and \( T_2 \), this implies that either \( r \) or \( s \) has no outgoing time-passage transitions. Hence, by rule \( \text{TIME} \), also \( r \parallel s \) has no outgoing time-passage transitions, that is \( a' \in E_r \). Assume \( a' = \tau \). It suffices to prove that \( b = 1 \). Since \( a' = \tau \) either rule \( \text{TAU} \) or rule \( \text{SYNC} \) has been used to prove \( r \parallel s \xrightarrow{a'.b} \). Assume w.l.o.g. that \( i = 1 \). If rule \( \text{TAU} \) is used and \( \text{Comm}(r) \) then \( b = 1 \) by Axiom I for \( T_1 \). If rule \( \text{TAU} \) is used and \( \text{Comm}(s) \) then \( b = 1 \) by definition. If rule \( \text{SYNC} \) is used then \( \text{Comm}(r) \lor \text{Comm}(s) \) implies \( b = 1 \).

2. Let \( r \parallel s \in S \) and let \( u \in \text{Val}(E) \). In order to prove Axiom II, we must show that \( (r \parallel s)[u] \in S \). By Lemma 1(10), \( r[u] \cap s[u] \) and by Lemma 1(11), \( (r \parallel s)[u] = r[u] \parallel s[u] \). Since \( T_1 \) and \( T_2 \) are compatible, \( r[u] = r[u][E_1] \) and \( s[u] = s[u][E_2] \). By Axiom II for \( T_1 \) and \( T_2 \), \( r[u][E_1] \in S_1 \) and \( s[u][E_2] \in S_2 \). Hence \( (r \parallel s)[u] \in S \).

3. In order to prove Axiom III, suppose \( r \parallel s \xrightarrow{c \cdot t, b} \) and \( u \in \text{Val}(E) \). We must establish that \( (r \parallel s)[u] \xrightarrow{c \cdot t, b} \). By rule \( \text{EXT} \), either \( r \xrightarrow{c \cdot t, b} \) or \( s \xrightarrow{c \cdot t, b} \). Assume w.l.o.g. that \( r \xrightarrow{c \cdot t, b} \). As in the previous case, we may infer \( r[u] \cap s[u] \), \( (r \parallel s)[u] = r[u] \parallel s[u] \), and \( r[u] = r[u][E_1] \). Hence, by Axiom III for \( T_1 \), \( r[u] \xrightarrow{c \cdot t, b} \). Using rule \( \text{EXT} \), we obtain \( (r \parallel s)[u] \xrightarrow{c \cdot t, b} \).

4. Axiom IV for \( T_1 \parallel T_2 \) follows trivially from Axiom IV for \( T_1 \) and \( T_2 \) and rule \( \text{TIME} \).

\[ \Box \]

Theorem 1. Composition of compatible TTSs is commutative and associative.

Proof. Commutativity is straightforward, using the symmetry in the definitions. For associativity observe that, if \( T_1 \), \( T_2 \) and \( T_3 \) are pairwise compatible, \( T_1 \parallel T_2 \) is compatible with \( T_3 \), and \( T_1 \) is compatible with \( T_2 \parallel T_3 \) (use Lemma 1(5)). Let \( T_L = (T_1 \parallel T_2) \parallel T_3 \) and \( T_R = T_1 \parallel (T_2 \parallel T_3) \). It is easy to see that \( T_L \) and \( T_R \) agree on all components, except for the transition relations. In order to prove that the set of transitions of \( T_R \) is contained in the set of transitions of \( T_L \), we distinguish 13 cases. The converse inclusion follows by a symmetric argument. The 13 cases correspond to the different ways in which an outgoing transition of a state \( r \parallel (s \parallel t) \) of \( T_R \) may be proved using the rules of Figure 2: a transition of the composed system may either be labeled by an external action originating from one of the components (3 cases), or by a \( \tau \) originating from one of the components (3 cases), or by a \( \tau \) that is the result of a synchronization between 2 components (6 cases, depending on who does the output and who does the input), or by a time-passage action (1 case). The various cases are denoted below in self-explanatory notation.

- Case (\( \epsilon \bullet \bullet \bullet \)). In this case \( r \xrightarrow{c \cdot b} r' \) and, by application of rule \( \text{EXT} \), \( r \parallel (s \parallel t) \xrightarrow{c \cdot b} r' \parallel (s \parallel t) \). Applying rule \( \text{EXT} \) twice gives a corresponding \( L \)-transition \( (r \parallel s) \parallel t \xrightarrow{c \cdot b} (r' \parallel s) \parallel t \). In fact, the \( R \) and \( L \)-transitions coincide since, by definition of \( \parallel \) and associativity of \( \parallel \), \( r' \parallel (s \parallel t) = (r' \parallel s) \parallel t \).
— Case (e • e). In this case $s \xrightarrow{c_{b}} s'$ and, by double application of rule EXT, $r \parallel (s\parallel t) \xrightarrow{s_{b}} (s' \triangleright t) \triangleright r$. Via another double application of rule EXT we derive the corresponding L-transition $(r \parallel s) \parallel t \xrightarrow{c_{b}} (s' \triangleright t) \triangleright r$. The two transitions coincide since, by associativity of $\triangleright$, definition of $\parallel$, and commutativity of $\parallel$,

$$(s' \triangleright t) \triangleright r = s' \triangleright (t \triangleright r) = s' \triangleright (s') \parallel t = (s' \triangleright t) \parallel r.$$  

— Case (• • e). In this case $t \xrightarrow{c_{b}} t'$ and, by double application of rule EXT, $r \parallel (s \parallel t) \xrightarrow{s_{b}} (t' \triangleright s) \triangleright r$. Via application of rule EXT we derive the corresponding L-transition $(r \parallel s) \parallel t \xrightarrow{t_{b}} (t' \triangleright s)$. The two transitions coincide since, by associativity of $\triangleright$, definition of $\parallel$, and commutativity of $\parallel$,

$$(t' \triangleright s) \triangleright r = t' \triangleright (s \triangleright r) = t' \triangleright (s \parallel t') = (t' \triangleright s) \parallel t.$$  

— Case (e • •). In this case $r \xrightarrow{c_{b}} r'$, Comm$(s||t) \Rightarrow b$ and, by application of rule TAU, $r \parallel (s||t) \xrightarrow{c_{b}} (r' \triangleright s || t)$. By Lemma 2 and propositional logic,

$$\text{Comm}(s||t) \Rightarrow b \iff (\text{Comm}(s) \Rightarrow b) \wedge (\text{Comm}(t) \Rightarrow b).$$  

Thus, by applying rule TAU twice, we may derive the corresponding L-transition $(r \parallel s) || t \xrightarrow{r_{b}} (r' \triangleright s \parallel t)$. The two transitions coincide since, as in case (e • e), $r \triangleright (s \parallel t) = (r \triangleright s) \parallel t$.

— Case (• • •). Similar to the previous case.

— Case (• • •). Similar to the pre-previous case.

— Case (c ? c •). In this case we have $r \xrightarrow{c_{1}} r'$, $s||t \xrightarrow{s_{b}} t$, and Comm$(r) \lor \text{Comm}(s||t) \Rightarrow b \lor b'$. By rule EXT, $s||t \xrightarrow{s_{b}} t \triangleright t[r']$ and, since $s||t \xrightarrow{s_{b}} t \triangleright t[r']$ by Lemma 1(11), application of rule SYNC gives $r \parallel (s||t) \xrightarrow{r_{b}} (s' \triangleright t[r'])$. By Lemma 2 and propositional logic,

$$\text{Comm}(r) \lor \text{Comm}(s||t) \Rightarrow b \lor b' \iff (\text{Comm}(r) \Rightarrow b) \lor (\text{Comm}(s||t) \Rightarrow b).$$  

Thus, by applying rule SYNC, we may derive a transition $r \parallel s \rightarrow b \lor b'$ and, by subsequent application of rule TAU, $(r \parallel s) \parallel t \xrightarrow{r_{b}} (r' \triangleright s') \triangleright t$. The $R$ and $L$-transition coincide since, by definition of $\triangleright$ and Lemma 1,

$$r' \triangleright (s' \triangleright t[r']) = (s' \triangleright t[r']) \triangleright r' = (s' \triangleright t[r']) \triangleright r' = s' \triangleright (r' \triangleright t) = (s' \triangleright r') \triangleright t = (r' \triangleright s') \triangleright t.$$  

— Case (• c c •). In this case $s \xrightarrow{c_{1}} s'$ and by rule EXT, $s||t \xrightarrow{s_{b}} t$. Moreover, $r[s \triangleright t] \xrightarrow{c_{1}} r'$, and Comm$(r) \lor \text{Comm}(s||t) \Rightarrow b \lor b'$. Since $||$
is commutative, rule \textbf{SYNC} gives \( r||(s||t) \xrightarrow{\tau,bv,b'} R (s' \triangleright t) \triangleleft r' \). As in the previous case,
\[
\text{Comm}(r) \lor \text{Comm}(s||t) \Rightarrow b \lor b' \iff
(\text{Comm}(r) \lor \text{Comm}(s) \Rightarrow b \lor b') \land (\text{Comm}(t) \Rightarrow b \lor b').
\]
Also observe that, by Lemmas 1(12) and 1(7),
\[
\begin{align*}
    r[s' \triangleright t] &= r[t][s'] = r[s']
\end{align*}
\]
Thus, by applying rule \textbf{SYNC} and using commutativity of \( || \), we may derive a transition \( r||s -' -V> > s' < r' \), and, by subsequent application of rule \textbf{TAU},
\[
(r||s)||t -' -V> > r'||(s'||t').
\]
The \( R \) and \( L \)-transition coincide since,
\[
(s' \triangleright t) \triangleleft r' = r' \triangleright (s' \triangleright t) = (r' \triangleright s') \triangleright t = (s' \triangleleft r') \triangleright t.
\]
— Case \((!c \bullet ?c)\). Similar to cases \((?c \bullet c \bullet)\) and \((!c \bullet c \bullet)\).
— Case \((?c \bullet !c)\). Similar to cases \((?c \bullet c \bullet)\) and \((!c \bullet c \bullet)\).
— Case \((c \bullet ?c)\). Similar to case \((?c \bullet c \bullet)\).
— Case \((c \bullet !c)\). Similar case \((!c \bullet c \bullet)\).
— Case \((d \; d \; d)\). In this case, \( r \xrightarrow{d} r', s \xrightarrow{d} s', \) and \( t \xrightarrow{d} t' \), for some \( d \in \mathbb{R}_{\geq 0} \),
and, by double application of rule \textbf{TIME}, \( r||(s||t) \xrightarrow{d} r'||(s'||t') \). By another double application of rule \textbf{TIME} we may derive the equivalent \( L \)-transition \( (r||s)||t \xrightarrow{d} (r'||s')||t' \).
\[\square\]

\section*{C Proof of Theorem 3}

\textbf{Theorem 2.} Let \( T_1, T_2, T_3 \) be TTSs with \( T_1 \) and \( T_2 \) comparable, \( T_1 \preceq T_2 \), and both \( T_1 \) and \( T_2 \) compatible with \( T_3 \). Then \( T_1\parallel T_3 \preceq T_2\parallel T_3 \).

\textbf{Proof.} Observe that, since \( T_1 \) and \( T_2 \) are comparable, \( T_1\parallel T_3 \) and \( T_2\parallel T_3 \) are comparable as well. Let \( T_{13} = T_1\parallel T_2 \) and \( T_{23} = T_2\parallel T_3 \). Let \( Q \) be a timed step simulation from \( T_1 \) to \( T_2 \). Define relation \( R \subseteq S_{13} \times S_{23} \) by
\[
q\parallel s \; R \; r\parallel s' \iff (q Q r \land s = s').
\]
We show that \( R \) is a timed step simulation from \( T_{13} \) to \( T_{23} \). First observe that \( (s_1\parallel s_3, s_2\parallel s_3) \in R(s_1\parallel s_3, s_2\parallel s_3) \) because \( s_1 Q s_2 \). For arbitrary \((q\parallel s, r\parallel s) \in R\), we prove that the four conditions in the definition of a timed step simulation are satisfied.

1. From \( q[E_1 = r[E_2] \equiv (q\parallel s)\parallel (r\parallel s)] \equiv (E_{13}) \)
2. Pick \( u \in \text{Val}(E_{13}) \) and let \( u' = u[E_1 \equiv (q\parallel s)[u] \equiv (r\parallel s)[u] \). Since \( Q \) is a timed step simulation, \( q[u'] \equiv (q\parallel s)[u] \equiv (r\parallel s)[u] \). Since \( T_3 \) is compatible with \( T_1 \) and \( T_2 \), \( q[u'] = q[u] \) and \( r[u'] = r[u] \). Thus \( q[u'] \equiv (q\parallel s)[u] \equiv (r\parallel s)[u] \) and therefore, by definition of \( R \), \( q[u]\parallel s[u] \equiv R(r\parallel s)[u] \). Hence \((q\parallel s)[u] \equiv R(r\parallel s)[u] \), using Lemma 1(11).
3. We derive

\[ \text{Comm}(r||s) \Rightarrow \text{Comm}(r) \vee \text{Comm}(s) \] (by Lemma 2)

\[ \Rightarrow \text{Comm}(q) \vee \text{Comm}(s) \] (Q is a timed step simulation)

\[ \Rightarrow \text{Comm}(q||s) \] (by Lemma 2)

4. Assume that \( q\parallel s \xrightarrow{a,b} q'||s' \). Via a case distinction on the rule instance from Figure 2 used to construct this transition, we establish that either there exists a transition \( r\parallel s \xrightarrow{a,b} r'||s'' \) such that \( q'||s' \rightarrow r'||s'' \) or \( a = \tau \) and \( q'||s' \rightarrow r||s \).

- **Rule EXT** with \( i = 1 \). Then \( a \in E \), \( q \xrightarrow{a,b}_1 q' \) and \( s' = s[q'] \). Since Q is a simulation, there exists a transition \( r \xrightarrow{a,b}_2 r' \) such that \( q' \rightarrow Q r' \). Let \( s'' = s[r'] \). Then \( q||s \xrightarrow{a,b} r'||s'' \). Since \( q' \rightarrow Q r' \), we know that \( q'[E_1] = r'[E_2] \). Hence \( s'' = s[q'] = s[r'] = s'' \) and we may infer \( q'||s' \rightarrow r'||s'' \), as requested.

- **Rule EXT** with \( i = 3 \). Then \( a \in E \), \( s \xrightarrow{a,b}_3 s' \) and \( q' = q'[s'] \). Let \( s'' = s' \) and \( r' = r[s'] \). Then \( r\parallel s \xrightarrow{a,b} r'||s'' \). Let \( u = s'[E_1] \). Since \( T_3 \) is compatible with \( T_1 \) and \( T_2 \), \( q[s'] = q[u] \) and \( r[s'] = r[u] \). Because Q is a simulation, \( q[u] \rightarrow Q r[u] \). Hence, \( q[u]\parallel s' \rightarrow Q r[u]\parallel s' \). This implies \( q'||s' \rightarrow r'||s'' \), as requested.

- **Rule TAU** with \( i = 1 \). Then \( a = \tau \), \( q \xrightarrow{a,b}_1 q' \), \( \text{Comm}(s) \Rightarrow b \) and \( s' = s[q'] \). Since Q is a simulation, either there exists a transition \( r \xrightarrow{a,b}_2 r' \) such that \( q' \rightarrow Q r' \), or \( q' \rightarrow Q r \).

  - In the first case, let \( s'' = s[r'] \). Then \( r\parallel s \xrightarrow{a,b} r'||s'' \) by rule TAU.
    Since \( q' \rightarrow Q r' \), we know that \( q'[E_1] = r'[E_2] \). Hence \( s'' = s[q'] = s[r'] = s'' \) and we may infer \( q'||s' \rightarrow r'||s'' \), as requested.
  
  - In the second case, where \( q' \rightarrow Q r \), observe that \( q[E_1] = r[E_2] = q'[E_1] \). Hence, \( q'[s'] = s[q'] = s \). Since Q is a simulation, \( q'[s'] \rightarrow Q r[s'] \). This implies \( q'||s' \rightarrow r'||s'' \), as requested.

- **Rule TAU** with \( i = 3 \). Then \( a = \tau \), \( s \xrightarrow{a,b}_3 s' \), \( \text{Comm}(q) \Rightarrow b \) and \( q' = q'[s'] \). Since Q is a simulation, \( \text{Comm}(r) \Rightarrow b \). Let \( s'' = s' \) and \( r' = r[s'] \). Then \( r\parallel s \xrightarrow{a,b} r'||s'' \). Let \( u = s'[E_1] \). Since \( T_3 \) is compatible with \( T_1 \) and \( T_2 \), \( q[s'] = q[u] \) and \( r[s'] = r[u] \). Because Q is a simulation, \( q[u] \rightarrow Q r[u] \). Hence, \( q[u]\parallel s' \rightarrow Q r[u]\parallel s' \). This implies \( q'||s' \rightarrow r'||s'' \), as requested.

- **Rule SYNC** with \( i = 1 \). Then \( a = \tau \) and, for some \( c \in C, b_1, b_2 \in B \), and \( \hat{q} \in S_1, \hat{q} \xrightarrow{c,b_1} \hat{q}, \hat{q}[s] \xrightarrow{c,b_2} s', b = b_1 \vee b_2 \), \( \text{Comm}(q) \lor \text{Comm}(s) \Rightarrow b \), and \( q' = q'[s'] \). Since Q is a simulation, there exists a state \( r_2 \in S_2 \) such that \( r \xrightarrow{c,b_2} r_2 \) and \( \hat{q} Q r_2 \). Moreover, \( \text{Comm}(r) \lor \text{Comm}(s) \Rightarrow b \).
  Since \( \hat{q} Q r_2 \), it follows that \( \hat{q}[E_1] = r_2[E_2] \). Since \( T_3 \) is compatible with \( T_1 \) and \( T_2 \), this implies that \( s[q] = s[r_2] \). Let \( s'' = s' \) and \( r' = r_2[s'] \).
  We can apply rule SYNC to infer that \( r\parallel s \xrightarrow{a,b} r'||s'' \). Since \( \hat{q} Q r_2 \) and \( T_3 \) is compatible with \( T_1 \) and \( T_2 \), it follows that \( \hat{q}[s'] \rightarrow Q r_2[s'] \). This implies \( q' \rightarrow Q r' \), which in turn implies \( q'||s' \rightarrow r'||s'' \), which in turn implies \( q'||s' \rightarrow r'||s'' \), as requested.
Rule SYNC with \( i = 3 \). Then \( a = \tau \) and, for some \( c \in C, b_1, b_2 \in B \), and \( \hat{s} \in S_3 \), \( s \xrightarrow{c,b_1} \hat{s}, q[\hat{s}] \xrightarrow{c,b_2} q' \), \( b = b_1 \lor b_2 \), \( \text{Comm}(q) \lor \text{Comm}(s) \Rightarrow b \), and \( s' = \hat{s}[q'] \). Since \( q \mathcal{Q} r \), \( Q \) is a simulation, and \( T_3 \) is compatible with \( T_1 \) and \( T_2 \), \( q[\hat{s}] \mathcal{Q} r[\hat{s}] \) and there exists a state \( r' \in S_2 \) such that \( r[\hat{s}] \xrightarrow{c,b_2} r' \) and \( q' \mathcal{Q} r' \). Moreover, \( \text{Comm}(r) \lor \text{Comm}(s) \Rightarrow b \). Let \( s'' = \hat{s}[r'] \). We can apply rule SYNC to infer that \( r\parallel s \xrightarrow{a,b} r'\parallel s'' \). Since \( q' \mathcal{Q} r' \), \( q'[E_1 = \tau][E_2 = \mathcal{Q}] \). Since \( T_3 \) is compatible with \( T_1 \) and \( T_2 \), it follows that \( \hat{s}[q'] = \hat{s}[r'] \). This implies \( s = s'' \), which in turn implies \( q' \parallel r'' \parallel s'' \), as requested.

Rule TIME. Then \( a \in \mathbb{R}_{\geq 0} \), \( b = 0 \), \( q \xrightarrow{a,b} q' \) and \( s \xrightarrow{a,b} s' \). Since \( Q \) is a simulation, there exists a transition \( r \xrightarrow{a,b} r' \) such that \( q' \mathcal{Q} r' \). Let \( s'' = s' \). Then \( r\parallel s \xrightarrow{a,b} r'\parallel s'' \) and \( q' \parallel s' \mathcal{Q} r' \parallel s'' \), as requested. \( \square \)

D Proofs from Section 4

Lemma 4. \( \text{TTS}(A) \) is a TTS.

Proof. Since \( A \) is a TA, \( E \) and \( H \) are disjoint. Hence, since \( \text{loc} \) is fresh, also \( E \) and \( H \cup \{\text{loc}\} \) are disjoint. By the definition of a timed automaton, \( v^0 \models I[\text{loc}] \). This implies \( s^0 \in S \), as required. We check that \( \text{TTS}(A) \) satisfies the four axioms for a TTS:

- Suppose that, for some state \( s \in S \), \( s \xrightarrow{a,1} s' \). Committed transitions can only be inferred using rule \( \text{ACT} \), and it follows that \( s(\text{loc}) \in K \). This implies that rule \( \text{TIME} \) can not be used to establish outgoing transitions from \( s \), and thus \( a' \in \varepsilon_r \). In fact, since outgoing transitions of \( s \) can only be established using rule \( \text{ACT} \), it follows that \( b = 1 \). This suffices to prove Axiom I.
- Axiom II follows directly from the fact that, by Axiom V, location invariants do not depend on external variables.
- Axiom III follows directly from the fact that, by Axiom VI, input guards do not depend on external variables.
- Axiom IV is immediate from rule \( \text{TIME} \). \( \square \)

The next technical lemma asserts that a state in the TTS semantics is committed iff the corresponding location is committed.

Lemma 5. Let \( A \) be a TA and let \( s \) be a state of \( \text{TTS}(A) \). Then \( s(\text{loc}) \in K \iff \text{Comm}(s) \).

Proof. Suppose that \( s(\text{loc}) \in K \). Let \( l = s(\text{loc}) \). By Axiom VII, \( A \) has a transition \( l \xrightarrow{g,a,p} l' \) such that \( s \models g \) and \( p(s) \models I[l'] \). This means that \( \text{TTS}(A) \) has a transition \( s \xrightarrow{a,1} s' \), where \( s' = p(s)[\{\text{loc} \rightarrow l'] \}. \) Hence \( \text{Comm}(s) \).

Now suppose that \( \text{Comm}(s) \). This means that \( s \) has an outgoing committed transition in \( \text{TTS}(A) \). But such a transition can only be derived using rule \( \text{ACT} \) provided \( s(\text{loc}) \in K \). \( \square \)
Theorem 4 Let $\mathcal{N} = \langle A_1, \ldots, A_n \rangle$ be an NTA. Then

$$\text{LTS}(\mathcal{N}) \cong \text{LTS}((\text{TTS}(A_1)) \cdots \text{TTS}(A_n)) \setminus \mathcal{C}.$$  

Proof. W.l.o.g. we assume that the fresh location variable of $\text{TTS}(A_m)$ is $\text{loc}_m$.\footnote{Without this assumption we need to drag around an isomorphism that takes care of appropriate renamings of location variables.}

It follows directly from the definitions that both sides of the equation have the same states and the same initial state. What remains is to prove that both sides also have the same set of transitions. Since the $\setminus \mathcal{C}$ operation prunes away all the external transitions, we need to prove $\subseteq$ as well as $\supseteq$ for two types of transitions, namely $\tau$-transitions and time-passage transitions.

$\subseteq \tau$-transitions. Assume $\mathcal{N}$ has a transition $s \xrightarrow{\tau} s'$. Write $s_m = s\lbrack W_m \rbrack$, for $1 \leq m \leq n$. According to Definition 8, transition $s \xrightarrow{\tau} s'$ is constructed either by rule TAU or rule SYNC.

**Rule TAU** For some $A_i$ all of the following hold.

- $l \xrightarrow{\tau} l'$
- $s(\text{loc}_i) = l$
- $s' = \rho(s)[\{\text{loc}_i \mapsto l'\}]$
- $s \models g$
- $b \land \forall k : s(\text{loc}_k) \notin K_k \lor l \in K_i$

Let $s'_i = \rho(s_i)[\{\text{loc}_i \mapsto l'\}]$ and $b \land \forall l \in K_i$. Then, by rule ACT of the TA semantics, $s_i \xrightarrow{\tau b} s'_i$. By associativity of parallel composition we may write $\text{TTS}(A_i) \parallel R$, where $R$ is the parallel composition of TTSs of all TAs except $A_i$. We define $\bar{s} = s\lbrack \bigcup_{m \neq i} W_m \rbrack$, that is, state $s$ restricted to the variables of $R$. Observe that

$$\text{Comm}(\bar{s}) \Rightarrow \text{Comm}(s_m) \text{ by Lemma 2}$$

$$\Rightarrow \exists m \neq i. s_m(\text{loc}_m) \in K_m \text{ by Lemma 5}$$

$$\Rightarrow \exists m. s_m(\text{loc}_m) \in K_m$$

$$\Rightarrow l \in K_i \text{ by assumption above}$$

$$\Rightarrow b$$

Hence we can apply rule TAU for parallel composition of TTSs

$$s_i \xrightarrow{\tau b} s'_i \text{ Comm}(\bar{s}) \Rightarrow b \text{ TAU}$$

and after applying $\setminus \mathcal{C}$ and LTS we obtain $s \xrightarrow{\tau} s'$, as required.

**Rule SYNC** For some $A_i$ and $A_j$ all of the following hold.

- $l_i \xrightarrow{g_i, \text{cl}_i} l'_i$
- $l_j \xrightarrow{g_j, \text{cl}_j} l'_j$
- $s' = \rho_j(\rho_i(s))[\{\text{loc}_i \mapsto l'_i, \text{loc}_j \mapsto l'_j\}]$
- $s \models g_i$
- $s \models g_j$
- $\forall k : s(\text{loc}_k) \notin K_k \lor l_i \in K_i \lor l_j \in K_j$
- $i \neq j$

Let $s_i = s\lbrack W_i \rbrack$ and $s_j = s\lbrack W_j \rbrack$.\footnote{Without this assumption we need to drag around an isomorphism that takes care of appropriate renamings of location variables.}
Clearly $s \cup s_j$.

Similarly to the previous case we get $s_i \xrightarrow{c_i, (l_i \in K_i)} s_i'$, where $s_i' = \rho_i(s_i)[\{\text{loc}_i \rightarrow l_i'\}]$.

By Axiom VI, $g_j$ does not depend on $E_j$, therefore $s_j(s_i] \models g_j$. Furthermore, clearly $s_j(s_i'](\text{loc}_j) = l_j$, and by TA semantics:

$$
\begin{align*}
\frac{\text{Comm}(s') = (\exists a \in \mathcal{E}_r : s_i \xrightarrow{\alpha -1} a)}{s_j'[s_i'](\text{loc}_j) = l_j} \\
\text{Comm}(s_i) \vee \text{Comm}(s_j) \Rightarrow (l_i \in K_i) \vee (l_j \in K_j).
\end{align*}
$$

Now by parallel composition:

$$
\begin{align*}
\frac{s_i \xrightarrow{c_i, (l_i \in K_i)} s_i'}{\text{Comm}(s_i) \vee \text{Comm}(s_j) \Rightarrow (l_i \in K_i) \vee (l_j \in K_j)}
\end{align*}
$$

By associativity of parallel composition we may write $\overline{\text{TTS}({A_i}) \parallel \text{TTS}({A_j})} \parallel R$, where $R$ is the parallel composition of TTSs of all TAs except $A_i$ and $A_j$. We define $\overline{s} = r[\bigcup_{m \notin \{i, j\}} W_m]$, the state $s$ without the variables solely used by $A_i$ or $A_j$. By parallel composition:

Now we will prove that $s_i' < s_j = s_i'[W_i \cup W_j]$. First we need the following identity, which is easy to derive:

$$
(\exists \overline{f} \in [V]) [\overline{f} = (\overline{f}[V])[g]]
$$

25
Now for the proof:

\[
\begin{align*}
\text{expand} & \quad \rho_j (\rho_i (s)) \left[ \{\text{loc}_i \rightarrow l'_{i}, \text{loc}_j \rightarrow l'_{j} \} \right] = \\
definition of \rho_j & \quad \rho_i (s) \left[ \rho_j (\rho_i (s) \left[ V_j \right]) \right] \left[ \{\text{loc}_i \rightarrow l'_{i}, \text{loc}_j \rightarrow l'_{j} \} \right] = \\
definition of \rho_i & \quad s \rho_i (s \left[ V_i \right]) \left[ \rho_j (\rho_i (s) \left[ V_j \right]) \right] \left[ \{\text{loc}_i \rightarrow l'_{i}, \text{loc}_j \rightarrow l'_{j} \} \right] = \\
\text{basic axiom} & \quad s \rho_i (s \left[ V_i \right]) \left[ \rho_j (\rho_i (s) \left[ V_j \right]) \right] \left[ \{\text{loc}_i \rightarrow l'_{i}, \text{loc}_j \rightarrow l'_{j} \} \right] = \\
\text{disjoint domains and reordering} & \quad s \rho_i (s \left[ V_i \right]) \left[ \{\text{loc}_i \rightarrow l'_{i} \} \right] \left[ \rho_j (\rho_i (s) \left[ V_j \right]) \right] \left[ \{\text{loc}_j \rightarrow l'_{j} \} \right] = \\
definitions \rho_i \text{ and } s_i & \quad s \rho_i (s_i) \left[ \{\text{loc}_i \rightarrow l'_{i} \} \right] \left[ \rho_j (\rho_i (s) \left[ V_j \right]) \right] \left[ \{\text{loc}_j \rightarrow l'_{j} \} \right] = \\
equivalent & \quad s \rho_j \left( s_i \left[ \rho_i (s) \left[ V_i \right] \right] \right) \left[ \{\text{loc}_j \rightarrow l'_{j} \} \right] = \\
definition of \rho_i & \quad s \rho_j \left( s_i \left[ \rho_i (s) \left[ V_i \right] \right] \right) \left[ \{\text{loc}_j \rightarrow l'_{j} \} \right] = \\
definitions \rho_i \text{ and } s_i & \quad s \rho_j \left( s_i \left[ \rho_i (s) \left[ V_i \right] \right] \right) \left[ \{\text{loc}_j \rightarrow l'_{j} \} \right] = \\
by s_i = s \left( V_i \cup \{\text{loc}_i \} \right) & \quad s \rho_j \left( s_i \left[ \rho_i (s) \left[ V_i \right] \right] \right) \left[ \{\text{loc}_j \rightarrow l'_{j} \} \right] = \\
deinition of \rho_i \text{ and } \rho_j & \quad s \rho_j \left( s_i \left[ \rho_i (s) \left[ V_i \right] \right] \right) \left[ \{\text{loc}_j \rightarrow l'_{j} \} \right] = \\
equivalent & \quad s \rho_j \left( s_i \left[ \rho_i (s) \left[ V_i \right] \right] \right) \left[ \{\text{loc}_j \rightarrow l'_{j} \} \right] = \\
equivalent & \quad s \rho_j \left( s_i \left[ \rho_i (s) \left[ V_i \right] \right] \right) \left[ \{\text{loc}_j \rightarrow l'_{j} \} \right] = \\
\end{align*}
\]

\[s' \quad \]
Finally from \( \text{LTS}(\cdot) \) we get \( s \xrightarrow{\tau} s' \).

\( \supseteq \tau \)-transitions. Assume \( s \xrightarrow{\tau} s' \) in \( \text{LTS}((\text{TTS}(A_1) \ || \ \cdots \ || \text{TTS}(A_n)) \setminus \mathcal{A}) \). By definition of \( \text{LTS}(\cdot) \) and \( \setminus \mathcal{A} \) there must be a transition \( s \xrightarrow{\tau,b} s' \) in \( \text{TTS}(A_1) \ || \ \cdots \ || \text{TTS}(A_n) \).

By parallel composition and its associativity we see this transition is constructed either by rule TAU or rule SYNC.

**Rule TAU** Some \( \text{TTS}(A_i) \) has transition \( s_i \xrightarrow{\tau,b} s'_i \), and \( s' = s[s'_i] \), where \( s_i = s[W_i] \). By rule ACT of TA semantics all of the following hold:

\[
\frac{s_i \parallel s_j \xrightarrow{\tau_i(l_i \in K_i) \lor (l_j \in K_j)} s'_i \triangleleft s'_j}{s \xrightarrow{\tau} s'} \quad \text{EXT}
\]

\[
\frac{(s_i \parallel s_j) \xrightarrow{\tau_i(l_i \in K_i) \lor (l_j \in K_j)} (s'_i \triangleleft s'_j) \triangleright \overline{s}}{s \xrightarrow{\tau} s'}
\]

From this we have the following:

- \( s[W_k] \) is the part of the state \( s \) that is determined by \( \text{TTS}(A_k) \). Now we have:

\[
\frac{s \notin K_i \Rightarrow \neg b}{s \notin K_j \Rightarrow \neg \text{Comm}(s)}
\]

- \( s \notin K_i \Rightarrow \neg \text{Comm}(s) \)

- \( \forall k : s((\text{lock}_k) \notin K_k) \) by TA semantics

- \( \neg (s_i \triangleleft g) \Rightarrow (s \triangleleft g) \)

Finally by NTA semantics we are done:

\[
\frac{s((\text{lock}_j) \xrightarrow{\tau_i,l'} s'_i = g_i \quad s'_i = \rho(s_i)((\text{lock}_i \leftarrow l')))}{s \xrightarrow{\tau_i,l'} s'_i \xrightarrow{\tau_i,l'} s}
\]

**Rule SYNC** Some \( \text{TTS}(A_i) \) and \( \text{TTS}(A_j) \), with \( i \neq j \) synchronize on transitions: \( s_i \xrightarrow{\tau_i,b_i} s'_i, \quad s_j \xrightarrow{\tau_j,b_j} s'_j \), where \( s_i = s[W_i], \quad s_j = s[W_j], \) and \( s' = s[s'_i \triangleleft s'_j] \).

and \( b = b_i \lor b_j \). By rule ACT of TA semantics all of the following hold:

\[
\frac{l_i \xrightarrow{g_i,\text{lock}_i} l'_i \quad s_i((\text{lock}_i) = l_i \quad s_i \triangleleft g_i \quad s'_i = \rho(s_i)((\text{lock}_i \leftarrow l'_i)) \quad b_i \Leftrightarrow (l_i \in K_i)}{s \xrightarrow{\tau} s'}
\]

\[
\frac{l_j \xrightarrow{g_j,\text{lock}_j} l'_j \quad s_j((\text{lock}_j) = l_j \quad s_j \triangleleft g_j \quad s'_j = \rho(s_j)((\text{lock}_j \leftarrow l'_j)) \quad b_j \Leftrightarrow (l_j \in K_j)}{s \xrightarrow{\tau} s'}
\]

From this we have the following:

- \( s[W_k] \) is the part of the state \( s \) that is determined by \( \text{TTS}(A_k) \). Now we have:

\[
\frac{(l_i \in K_i \lor l_j \in K_j) \Rightarrow \neg b}{\neg \text{Comm}(s)}
\]

- \( \forall k : s((\text{lock}_k) \notin K_k) \) by TA semantics
\[ s_i(\text{loc}_i) = l_i \Rightarrow s(\text{loc}_i) = l_i \]
\[ s_j \models g_j \Rightarrow s \models g_j \]
\[ s_j[s_i'](\text{loc}_j) = l_j \Rightarrow s(\text{loc}_j) = l_j \]
\[ (s_j[s_i'] \models g_j) \Rightarrow (s \models g_j) \]

By NTA semantics:
\[
\begin{array}{c}
\begin{align*}
\lambda_i & \xrightarrow{g_i} \lambda_i' \\
\lambda_j & \xrightarrow{g_j} \lambda_j'
\end{align*}
\end{array}
\]
\[
\begin{array}{c}
\begin{align*}
\rho_j(\rho_i(s)) & \models \{\text{loc}_{i'} \mapsto l'_{i}, \text{loc}_{j'} \mapsto l'_{j}\} \\
\end{align*}
\end{array}
\]

Finally \(\rho_j(\rho_i(s))\{\text{loc}_{i'} \mapsto l'_{i}, \text{loc}_{j'} \mapsto l'_{j}\} = s[s_i' < s_j']\), using the proof on page 26.

\(\subseteq\) time-passage transitions. Assume a transition \(s \xrightarrow{d} s'\). By rule TIME of NTA semantics all of the following hold:
\[
\begin{array}{c}
\begin{align*}
s' & = s \oplus d \\
\forall k : s(\text{loc}_k) \notin K_k & \Rightarrow \#(l \xrightarrow{g \cdot \tau \cdot \rho} l') : s(\text{loc}_i) = l \land s \models g
\end{align*}
\end{array}
\]  
(14)

We proceed our proof by induction on the number of timed automata that are put in parallel. In case \(n = 1\), \((\forall k : s(\text{loc}_k) \notin K_k) \Leftrightarrow s(\text{loc}_1) \notin K_1\), and by TA semantics:
\[
\begin{array}{c}
\begin{align*}
s' & = s \oplus d \\
s(\text{loc}_1) & \notin K_1 \\
\#(l \xrightarrow{g \cdot \tau \cdot \rho} l') : s(\text{loc}_1) = l \land s \models g
\end{align*}
\end{array}
\]

Finally by definition of LTS(-), we have \(s \xrightarrow{d} s'\), the transition we needed.

Now assume the theorem holds for \(m - 1\), we will prove it holds for \(m\). We define \(s = s[(W_1 \cup \cdots \cup W_{m-1})]\), the state \(s\) without the variables solely used by \(A_m\). We define \(s_m = s[W_m]\).

Equation (14) implies the premises of rule TIME of TA semantics, so:
\[
\begin{array}{c}
\begin{align*}
s_m' & = s_m \oplus d \\
s_m(\text{loc}_{m}) & \notin K_m \\
\#(l \xrightarrow{g \cdot \tau \cdot \rho} l') : s_m(\text{loc}_m) = l \land s_m \models g
\end{align*}
\end{array}
\]

TIME

By the induction hypothesis there exists a transition \(s_m \xrightarrow{d,0} s_m'\) in \(\text{LTS}((\text{TTS}(A_1))\cdots\text{TTS}(A_{m-1}))\}\backslash C\).
By definition of \(\text{LTS}(-)\) and \(\backslash C\), there is a transition \(\bar{s} \xrightarrow{d,0} \bar{s}'\) in \(\text{TTS}(A_1)\cdots\text{TTS}(A_{m-1})\)

By parallel composition we get the transition we need:
\[
\begin{array}{c}
\begin{align*}
\bar{s} & \xrightarrow{d} \bar{s}' \\
\bar{s} & \xrightarrow{d} \bar{s}'
\end{align*}
\end{array}
\]

TIME

Finally from \(\text{LTS}(-)\) we get \(s \xrightarrow{d} s'\).
\[ \geq \text{time-passage transitions. Assume } s \xrightarrow{d} s' \text{ in } \text{LTS((TTS}(A_1)\| \cdots \| \text{TTS}(A_n))\setminus \mathcal{C}). \]

By definition of \text{LTS}(\cdot) and \setminus \mathcal{C} there must be a transition \( s \xrightarrow{d_0} s' \) in \( \text{TTS}(A_1)\| \cdots \| \text{TTS}(A_n) \).

We proceed our proof by induction on the number of timed automata that are put in parallel. In case \( n = 1 \), by rule \text{TIME} of TA semantics we the premises of rule \text{TIME} of NTA semantics, so:

\[
s' = s \oplus d \quad \forall k : s(\text{loc}_k) \notin K_k \quad \exists l : (l \overset{\text{if } s(\text{loc}_k) = l}{\xrightarrow{u}} l') : s(\text{loc}_k) = l \land s \models g \quad \text{TIME}
\]

Now assume the theorem holds for \( m - 1 \), we will prove it holds for \( m \). We define \( s = s[(W_1 \cup \cdots \cup W_{m-1})] \), the state \( s \) without the variables solely used by \( A_m \). We define \( s_m = s\lceil W_m \).

By associativity of parallel composition we can write:

\[
(\text{TTS}(A_1)\| \cdots \| \text{TTS}(A_{m-1}))\| \text{TTS}(A_m)
\]

By rule \text{TIME} of parallel composition we have the transitions: \( s_m \xrightarrow{d_0} s'_m \)

in \( \text{TTS}(A_m) \), and \( s \xrightarrow{d_0} s' \) in \( \text{TTS}(A_1)\| \cdots \| \text{TTS}(A_{m-1}) \).

By TA semantics all of the following hold:

\[
\begin{align*}
    s_m(\text{loc}_m) & \notin K_m \quad \exists l : (l \overset{\text{if } s_m(\text{loc}_m) = l}{\xrightarrow{u}} l') : s_m(\text{loc}_m) = l \land s_m \models g \\
\end{align*}
\]

From the induction hypothesis we know there exists a transition \( s \xrightarrow{d} s' \) in the semantics of the NTA made up of automata \( A_1, \ldots, A_{m-1} \). Now by rule \text{TIME} of NTA semantics all of the following hold:

\[
\begin{align*}
    \forall k : s(\text{loc}_k) \notin K_k \quad \exists l : (l \overset{\text{if } s(\text{loc}_k) = l}{\xrightarrow{u}} l') : s(\text{loc}_k) = l \land s \models g \\
\end{align*}
\]

Together equations (15) and (16) imply the premises of rule \text{TIME} of NTA semantics, so finally:

\[
\begin{align*}
    s' = s \oplus d \quad \forall k : s(\text{loc}_k) \notin K_k \quad \exists l : (l \overset{\text{if } s(\text{loc}_k) = l}{\xrightarrow{u}} l') : s(\text{loc}_k) = l \land s \models g \\
    s \xrightarrow{d_{\mathcal{N}}} s'
\end{align*}
\]

\( \square \)