Using Task-Structured Probabilistic I/O Automata to Analyze Cryptographic Protocols

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Abstract. The Probabilistic I/O Automata (PIOA) framework of Lynch, Segala and Vaandrager provides tools for precisely specifying protocols and reasoning about their correctness based on implementation relationships between multiple levels of abstraction.

We enhance this framework to allow the analysis of protocols that use cryptographic primitives. For this purpose, we propose new techniques for handling nondeterministic behaviors, expressing computationally hardness assumptions, and for proving security in a composable setting.

1 Introduction

The task of modeling and analyzing of cryptographic protocols is typically complex, involving many subtleties and details, even when the analyzed protocols are simple. This causes security analysis of cryptographic protocols to be susceptible to errors and omissions (see [1–3] for instance). Our goal is to present a method for analyzing cryptographic protocols rigorously and systematically in a composable framework, while taking into account computational issues regarding cryptographic primitives.

This work is most closely related to the efforts of Backes, Pfitzmann and Waidner [4, 5], and of Lincoln, Mateus, Mitchell, Mitchell, Ramanathan, Scedrov and Teague [6, 7]. The main conceptual difference between these works and the current one lies in the way we handle nondeterminism, as we will see below.1

1 A detailed comparison of our framework with the one of Backes & al. [4] is available in [8, 9].
2 Task-PIOAs

PIOAs Our approach is based on an extension of the Probabilistic I/O Automata (PIOA) framework developed in the concurrency semantics research community [10, 11]. Briefly, a PIOA is a kind of abstract automaton. It includes states, start states, input, output, and internal actions. Each action has an associated set of transitions, which go from states to probability distributions on states. PIOAs are capable of expressing both probabilistic choices and nondeterministic choices. PIOAs that model individual components of a system may be composed to yield a PIOA model for the entire system.

Task-PIOAs Traditionally, centralized, perfect-information schedulers are used to resolve nondeterministic choices in a PIOA. Such a scheduler has full knowledge about the past execution and is too powerful for computational analysis of security protocols: it might provide covert channels by scheduling actions of adversarial components as a function of secrets of protocol parties. To address this issue, we propose a distinction between high- and low-level nondeterminism. High-level nondeterminism refers to the adversarially observable events, for instance, message transmission on the network. This type of nondeterminism, which is standard in the cryptographic community, is algorithmically resolved by the automaton representing the adversary. Low-level nondeterminism refers to the ordering of events that are not controlled by adversarial components, e.g., internal or output transitions of protocol parties. As observed in the concurrency community, this type of nondeterminism is quite useful in protocol specification: by leaving the ordering of events unspecified whenever possible, we reduce the amount of inessential details (the so-called “clutters”) contained in our models. This often simplifies correctness proofs involving implementation relations. More importantly, the resulting correctness statement is more general, because it is valid no matter how the nondeterministic choices are resolved in a real-life implementation. Thus, an implementer has more freedom to make design decisions based on the specific context in which the protocol is used (e.g., hardware and network characteristics). We think that capturing and separating these two types of nondeterminisms is an aspect in which our approach differs from all existing cryptographic frameworks.

To resolve low-level nondeterminism in a consistent manner, we extend the PIOA framework with a new task mechanism, obtaining task-PIOAs as detailed in [8]. Basically, a task-PIOA is a pair \((\mathcal{P}, R)\) where \(\mathcal{P}\) is a classical PIOA and \(R\) is an equivalence relation on internal and output actions of \(\mathcal{P}\). Each equivalence class of \(R\) is called a task, and we require that tasks satisfy the action determinism axiom, which states that, given a state and a task of a task-PIOA, there is at most one (probabilistic) action of that task that is enabled in that state. We use tasks to abstract from the actual state variables of the protocol parties: for instance, a task will be “send first protocol message”, without reference to a specific message content. Given this task mechanism, we define schedulers as simply an arbitrary sequence of tasks, called a task scheduler. The
action determinism property of tasks guarantees that specifying a task scheduler for a given task-PIOA resolves all nondeterministic choices and defines a purely probabilistic execution.

**Time-bounded Task-PIOAs** The security of cryptographic protocols typically relies on the assumption that certain problems cannot be solved with nonnegligible probability by resource bounded entities. In order to capture these bounds, we define *time-bounded* task-PIOAs [9]. Basically, a task-PIOA $T$ is $b$-time-bounded if, assuming a standard bit representation, (i) all its components (state, actions, tasks, ...) can be represented as bit strings of length at most $b$, (ii) there is a Turing machine that can decide in time at most $b$ if a bit string is the representation of a task-PIOA component (iii) given a task and a state, there is a Turing machine that can determine in time at most $b$ the unique enabled action (if there is one), (iv) given a state and an action, there is a Turing machine that can compute in time at most $b$ the next state of $T$. Furthermore, we require that all these Turing machines can be described as bit strings of length at most $b$, given some standard encoding.

Typically, a computational hardness assumption states that, as the size of a problem grows, the success probability of a resource-bounded entity trying to solve the problem diminishes quickly. The size of a problem is expressed in terms of a *security parameter* $k \in \mathbb{N}$. Accordingly, we define families of task-PIOAs indexed by a security parameter: a *task-PIOA family* $\mathcal{T}$ is an indexed set $\{T_k\}_{k \in \mathbb{N}}$ of task-PIOAs. The notion of time bound is also expressed in terms of the security parameter; namely, given $b : \mathbb{N} \to \mathbb{R}^\geq 0$, we say that $\mathcal{T}$ is $b$-bounded if every $T_k$ is $b(k)$ time-bounded. Also, we say that a family $\mathcal{T}$ is polynomial-time-bounded if it is bounded by a polynomial function.

### 3 Proving Security

**Defining Security** We perform security analysis along the lines of Universally Composable Security [12] and Reactive Simulatability [5]. Following these approaches, the functionality to be achieved by a protocol is described by a task-PIOA family $\mathcal{F}$, which typically models a kind of trusted party that computes the correct result from given inputs. A protocol $\mathcal{P}$ is defined to be secure if, for any adversary $\mathcal{A}$ that interacts with the protocol, there exists a “simulator” $\mathcal{S}$ that interacts with the functionality such that no external environment can distinguish whether it is interacting with the protocol and $\mathcal{A}$ or, with the functionality and $\mathcal{S}$. In the task-PIOA framework we express this indistinguishability notion by saying that the composition of $\mathcal{P}$ and $\mathcal{A}$ must implement the composition of $\mathcal{F}$ and $\mathcal{S}$, which we denote by $\mathcal{P} \parallel \mathcal{A} \leq_{\text{neg,pt}} \mathcal{F} \parallel \mathcal{S}$. More precisely, the $\leq_{\text{neg,pt}}$ implementation relation means that, for every polynomial time-bounded environment family $\mathcal{E}$ and every polynomial length-bounded task scheduler for $\mathcal{P} \parallel \mathcal{A} \parallel \mathcal{E}$, there is a polynomial length-bounded task scheduler for $\mathcal{F} \parallel \mathcal{S} \parallel \mathcal{E}$ such that the probabilities that $\mathcal{E}$ performs an accept output actions in these two systems differ by a negligible amount. An important part of this definition is
that it includes the quantifiers on the task-schedulers, that is, we prove security properties for every way to resolve the low-level nondeterminism. In [9], we prove convenient properties of the $\leq_{\text{neg,pt}}$ relation; for example, it is transitive and is preserved when we compose two $\leq_{\text{neg,pt}}$-related systems with any polynomial-time-bounded task-PIOA family.

**Proving Security** In order to prove that two task-PIOA families $A$ and $B$ are $\leq_{\text{neg,pt}}$ related, we decompose our security proofs in several steps, as in game-based proofs [13, 14]: in order to prove that $A \leq_{\text{neg,pt}} B$, we prove that $A_1 \leq_{\text{neg,pt}} \cdots \leq_{\text{neg,pt}} A_n$, where $A_1 = A$ and $A_n = B$. The families $A_i$ and $A_{i+1}$ are defined in such a way that, either they are perfectly indistinguishable, that is, indistinguishable even by an unbounded environment, or they only differ by a small detail corresponding to a computational assumption.

When $A_i$ and $A_{i+1}$ are perfectly indistinguishable, we prove that $A_i \leq_{\text{neg,pt}} A_{i+1}$ by using a new, sound, simulation relation [8, 9]. Even though the form of this simulation relation is not usual (for instance, it relates probability distribution on executions rather than states), this type of proof requires using fairly traditional formal methods.

In order to relate systems that are indistinguishable in a computational sense only, we translate computational hardness assumptions in terms of implementation relations between task-PIOA families: for instance, for expressing the DDH assumption, we define a family $\text{DDH}_1$ transmitting a triple $(g^x, g^y, g^{xy})$, a family $\text{DDH}_2$ transmitting a triple $(g^x, g^y, g^z)$ (where $x$, $y$, and $z$ are selected randomly), and claim that $\text{DDH}_1 \leq_{\text{neg,pt}} \text{DDH}_2$. In [9], we prove for a similar case that this formulation style is equivalent to the classical computational definition.

In order to exploit these computational assumptions, we define $\overline{A}_i$ and $\overline{A}_{i+1}$ in such a way that they can be expressed as $C_1 || \text{Ifc}$ and $C_2 || \text{Ifc}$ respectively, where $C_1 \leq_{\text{neg,pt}} C_2$ is a stated computational assumption and $\text{Ifc}$ is polynomial-time-bounded (the $\text{Ifc}$ family plays the role of the reduction in classical computational proofs). Now, the relation $\overline{A}_i \leq_{\text{neg,pt}} \overline{A}_{i+1}$ follows from the composition property of the $\leq_{\text{neg,pt}}$ relation.

As a case-study, we used these techniques in [9] for the analysis of a classical Oblivious Transfer protocol [15]. This analysis involves a passive adversary, as the OT protocol we consider is only secure in front of this type of opponent. This passive adversary is modeled by restricting the task-PIOA describing the adversary to only send messages he previously received. Dealing with an active adversary would simply correspond to removing this restriction in the adversary definition.

4 Conclusion

When working on our OT case-study, we found that breaking down our proofs into several pieces in order to separate issues of probability from computational issues was specially convenient: probability issues can be dealt with using fairly
traditional formal methods, while computational issues are concentrated on iso-
lated pieces, which can be managed independently with traditional cryptographic
 techniques: formulating computational assumptions, and building reductions.
 This should provide a way for people from the formal and computational cryp-
tography communities to work together on proofs in a single framework.

We used our task-PIOA framework to establish composable security proofs.
So, we expect that it can eventually be used to obtain sound abstractions for
classical cryptographic primitives and protocols. This would allow performing
sound, symbolic-style, analysis inside our framework: symbolic analysis of cryp-
tographic protocols based on I/O automata has already been performed in [16].

Our plans for the near future include establishing general composition theo-
rems in the style of [12, 5], to model more sophisticate computational assump-
tions, and to use these results for the analysis of a key exchange protocol.

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