Switched Probabilistic I/O Automata

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Abstract. A \textit{switched probabilistic I/O automaton} is a special kind of probabilistic I/O automaton (PIOA), enriched with an explicit mechanism to exchange control with its environment. Every closed system of switched automata satisfies the key property that, in any reachable state, at most one component automaton is active. We define a trace-based semantics for switched PIOAs and prove it is compositional. We also propose \textit{switch extensions} of an arbitrary PIOA and use these extensions to define a new trace-based semantics for PIOAs.

1 Introduction

Probabilistic automata [Seg95,Sto02] constitute a mathematical framework for modeling and analyzing probabilistic systems, specifically, systems consisting of asynchronously interacting components capable of nondeterministic and probabilistic choices. This framework has been successfully adopted in the studies of distributed algorithms [LSS94,PSL00,Agg94] and practical communication protocols [SV99].

An important part of such a framework is a notion of \textit{visible behavior} of system components. This is used to derive implementation and equivalence relations among components. For example, one can define the visible behavior of a nondeterministic automaton to be its set of \textit{traces} (i.e., sequences of visible actions that arise during executions of the automaton [LT89]). This induces
an implementation (resp. equivalence) relation on nondeterministic automata, namely inclusion (resp. equality) of sets of traces.

Perhaps the most important property of an implementation relation is compositionality: if \( P \) implements \( Q \), then for every context \( R \), one should be able to infer that \( P \parallel R \) implements \( Q \parallel R \). This property facilitates correctness proofs of complex systems by reducing properties of a large system to properties of smaller subsystems. In the setting of security analysis, for instance, compositionality ensures that plugging secure components into a security preserving context results again in a secure component [Can01].

Generalizing the notion of traces, Segala [Seg95] defines the visible behavior of a probabilistic automaton as its set of trace distributions, where each trace distribution is induced by a probabilistic scheduler which resolves all nondeterministic choices. This gives rise to implementation and equivalence relations as inclusion and equality of sets of trace distributions, respectively. It turns out that this notion of implementation relation is not compositional. A simple counterexample is illustrated in Figure 1.

![Fig. 1. Probabilistic automata Early, Late and Toss](image)

As their names suggest, automaton Early forces its scheduler to choose between \( b \) and \( c \) as it chooses one of the two available \( a \)-transitions, whereas automaton Late allows its schedulers to make this decision after the \( a \)-transition. Clearly, these two automata have the same set of trace distributions, but they can be distinguished by the context Toss. The composed system Late \( \parallel \) Toss has a trace distribution \( \mathcal{D}_0 \) that assigns probability \( \frac{1}{2} \) to each of these traces: \( adb \) and \( aec \). Such total correlations between actions \( d \) and \( b \), and between actions \( e \) and \( c \), cannot be achieved by the composite Early \( \parallel \) Toss.

Inspired by this example, we establish in [LSV03] that the coarsest precongruence refining trace distribution preorder coincides with the probabilistic simulation preorder. In other words, probabilistic contexts are capable of exposing internal branching structures of other components.

Aside from its inspirational merits, this example reveals an unsatisfactory aspect of the composition mechanism of probabilistic automata. Namely, nondeterministic choices are resolved after the two automata are composed, allowing the global scheduler to make decisions in one component using state information of the other. This phenomenon can be viewed as a form of “information leak-
age**: the global scheduler channels private information from one component to the other, in particular, from Toss to Late.

In this paper, we present a composition mechanism where local scheduling decisions are based on strictly local information. That is, (i) local nondeterministic choices of each component are resolved by that component alone; (ii) global nondeterministic choices (i.e., inter-component choices) are resolved by some independent means. To address the first issue, we introduce an input/output distinction to our model and pair each automaton with an input/output scheduler. For the second, we introduce a control-passage mechanism, which eliminates global scheduling conflicts.

Before describing our model in greater detail, we take a quick look at related proposals in the existing literature. (We refer to [SV04] for a comparative study of various probabilistic models.) For purely synchronous, variable-based models, global nondeterministic choices are resolved by “avoidance”: in each transition of the global system, all components may take a step. This intrinsic feature of synchronous models allows De Alfaro, Henzinger and Jhala [dAHJ01] to successfully define a compositional, trace-based semantics for their model of probabilistic reactive modules. For asynchronous models such as probabilistic automata, global nondeterministic choices must be resolved explicitly in order to assign a probability mass to each possible interleaving of actions. Wu, Smolka and Stark [WSS94] propose a compositional model based on probabilistic input/output automata. In that model, global nondeterminism is resolved by a “race” among components: each component draws a delay from an exponential distribution (thus leaving the realm of discrete distributions). Assuming independence of these random draws, the probability of two components drawing the same delay is zero, therefore there is almost always a unique winner.

In this paper, we introduce the model of **switched probabilistic I/O automata** (or switched automata for short). This augments the probabilistic I/O automata model with some additional structures and axioms. In particular, we add a predicate active on the set of states, indicating whether the automaton is active or inactive. We require that locally controlled actions are enabled only if the automaton is active. In other words, an inactive automaton must be quiescent and can only accept inputs from the environment.

A switched automaton changes its activity status by performing special control input and control output actions. Control inputs switch the machine from inactive to active and vice versa for control outputs. All other actions must leave the activity status unchanged. It is important that all control communications are “handshakes”: at most two components may participate in a synchronization labeled by a control action. Together with an appropriate initialization condition, this ensures that at most one component is active at any point of an execution. Intuitively, we model a network of processes passing a single token among them,

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1 Throughout this paper, the term control is used in the spirit of “control flow” in sequential programming; a component is said to possess the control of a system if it is scheduled to actively perform the next action. This should not be confused with the notion of controllers for plants, as in control theory.
with the property that a process enables a locally controlled transition if and only if it possesses the token.

The main technical result of this paper is compositionality of a trace-based semantics for switched probabilistic I/O automata (Section 6, Theorem 1). Sections 2 and 3 are devoted to the basic theory. There we introduce new technical notions such as I/O schedulers, scheduled automata and parallel composition of scheduled automata. In Section 4, we define pseudo probabilistic executions and pseudo trace distributions for automata with open inputs, and prove important projection and pasting results. Section 5 treats two standard operators: renaming and hiding. In Section 7, we propose the notion of switch extensions for PIOAs, which can be used to derive a new form of composition for the original PIOA model. Concluding discussions follow in Section 8. Due to space constraints, we have omitted many proofs. These can be found in a full version of this paper available at http://www.cs.kun.nl/ita/publications/papers/fvaan/switched.html.

2 Preliminaries

In this section, we define probabilistic I/O automata and some related notions. This is a straightforward combination of the Input/Output Automata model of Lynch and Tuttle [LT89] and the Simple Probabilistic Automata model of Segala [Seg95].

A discrete probability (resp. sub-probability) measure over a set $X$ is a measure $\mu$ on $(X, 2^X)$ such that $\mu(X) = 1$ (resp. $\mu(X) \leq 1$). With slight abuse of notation, we write $\mu(x)$ for $\mu(\{x\})$. The set of all discrete probability measures over $X$ is denoted $\text{Disc}(X)$; similarly for $\text{SubDisc}(X)$. Moreover, we use $\text{Supp}(\mu)$ to denote the support of a discrete measure $\mu$: the set of elements in $X$ to which $\mu$ assigns nonzero measure. Given $x \in X$, the Dirac distribution on $x$ is the unique measure assigning probability 1 to $x$, denoted $(x \mapsto 1)$.

A probabilistic I/O automaton (PIOA) $P$ consists of:

- a set $\text{States}(P)$ of states and a start state $s^0 \in \text{States}(P)$;
- a set $\text{Act}(P)$ of action symbols, partitioned into: $I$ (input actions), $O$ (output actions) and $H$ (hidden actions);
- a transition relation $\rightarrow \subseteq \text{States}(P) \times \text{Act}(P) \times \text{Disc}(\text{States}(P))$.

An action is visible if it is not hidden. It is locally controlled if it is non-input (i.e., either output or hidden); we define $L := O \cup H$. We write $s \xrightarrow{a} \mu$ for $(s, a, \mu) \in \rightarrow$, and $s \xrightarrow{a} s'$ if there exists $\mu$ with $s \xrightarrow{a} \mu$ and $s' \in \text{Supp}(\mu)$. A state is quiescent if it enables only input actions. A PIOA is closed if its set of input actions is empty. As with I/O automata, we always assume input enabling: $\forall s \in \text{States}(P) \forall a \in I \exists \mu : s \xrightarrow{a} \mu$.

An execution of $P$ is a (possibly finite) sequence $p = s_0 a_1 s_1 a_2 s_2 \ldots$, such that:

- each $s_i$ (resp., $a_i$, $\mu_i$) denotes a state (resp., action, distribution over states);
In some literature, executions are defined to be sequences of states and actions in alternating fashion, thus omitting the target distributions. We adopt the current style for a more straightforward generalization to probabilistic executions.

Given a finite execution $p$, we use $\text{last}(p)$ to denote the last state of $p$. A state $s$ is \textit{reachable} if there exists a finite execution $p$ such that $\text{last}(p) = s$. We write $\text{Exec}(P)$ for the set of all executions of $P$ and $\text{Exec}^{\omega}(P)$ for the set of finite executions. Given an execution $p$, the sequence of visible action symbols in $p$ is called the \textit{(visible) trace} of $p$, denoted $\text{tr}(p)$.

A finite set of PIOAs $\{P_1, \ldots, P_n\}$ is said to be \textit{compatible} if for all $i \neq j$, $O_i \cap O_j = \{\text{Act}(P_i) \cap H_j \neq \emptyset\}$. Such a set is \textit{closed} if $\bigcup_{1 \leq i \leq n} O_i \subseteq \bigcup_{1 \leq i \leq n} O_i$. We define $P = \prod_{1 \leq i \leq n} P_i$ as usual with synchronization of shared actions:

- $\text{States}(P) := \prod_{1 \leq i \leq n} \text{States}(P_i)$ and the start state of $P$ is $\langle s_1^0, \ldots, s_n^0 \rangle$;
- $I := \bigcup_{1 \leq i \leq n} I_i \setminus \bigcup_{1 \leq i \leq n} O_i$, $O := \bigcup_{1 \leq i \leq n} O_i$, and $H := \bigcup_{1 \leq i \leq n} H_i$;
- given a state $\langle s_1, \ldots, s_n \rangle$, an action $a$ and a target distribution $\mu$, there is a transition $\langle s_1, \ldots, s_n \rangle \xrightarrow{a} \mu$ if and only if $\mu$ is of the form $\mu_1 \times \ldots \times \mu_n$ and for all $1 \leq i \leq n$,
  - either $a \in \text{Act}(P_i)$ and $s_i \xrightarrow{a} \mu_i$,
  - or $a \not\in \text{Act}(P_i)$ and $\mu_i = (s_i \mapsto 1)$.

Notice $\prod$ is commutative and associative for PIOAs (up to isomorphism).

The notion of (probabilistic) schedulers for a PIOA $P$ is introduced as a means to resolve all nondeterministic choices in $P$. Each scheduler consists of an input component and an output component. Given a finite history of the automaton, the output scheduler chooses probabilistically the next locally controlled transition, whereas the input scheduler responds to inputs from the environment and chooses probabilistically a transition carrying the correct input symbol.

**Definition 1.** An \textit{input scheduler} $\sigma$ for $P$ is a function

$$
\sigma : \text{Exec}^{\omega}(P) \times I \longrightarrow \text{Disc}(\rightarrow)
$$

such that for all $(p, a) \in \text{Exec}^{\omega}(P) \times I$ and transitions $(s \xrightarrow{b} \mu) \in \text{Supp}(\sigma(p, a))$, we have $s = \text{last}(p)$ and $b = a$. An \textit{output scheduler} $\rho$ for $P$ is a function

$$
\rho : \text{Exec}^{\omega}(P) \longrightarrow \text{SubDisc}(\rightarrow)
$$

such that for all $p \in \text{Exec}^{\omega}(P)$ and transition $(s \xrightarrow{a} \mu) \in \text{Supp}(\rho(p))$, we have $s = \text{last}(p)$ and $a \in L$. An I/O scheduler for $P$ is then a pair $(\sigma, \rho)$ where $\sigma$ is an input scheduler for $P$ and $\rho$ is an output scheduler for $P$.

Notice input schedulers must return a discrete probability distribution, reflecting the requirement that each input issued by the environment is received
with probability 1. (This is always possible because of the input enabling assumption.) In contrast, output schedulers may choose to halt with an arbitrary probability \( \theta \) by returning a proper sub-distribution whose total probability mass is \( 1 - \theta \). Finally, we write \( \sigma(p,a)(\mu) \) as a shorthand for \( \sigma(p,a)(\text{last}(p) \xrightarrow{a} \mu) \) and \( \rho(p)(a,\mu) \) for \( \rho(p)(\text{last}(p) \xrightarrow{a} \mu) \).

Consider a closed PIOA \( P \). Obviously, any I/O scheduler for \( P \) has a trivial input component (i.e., the empty function). Every output scheduler \( \rho \) thus induces a purely probabilistic behavior, which is captured by the following notion of probabilistic executions. The **probabilistic execution** induced by \( \rho \) is the function \( Q_\rho : \text{Exec}^{<\omega}(P) \rightarrow [0,1] \) defined recursively by:

\[
\begin{align*}
Q_\rho(s^0) &:= 1, \text{ where } s^0 \text{ is the initial state of } P; \\
Q_\rho(p') &:= Q_\rho(p) \cdot \rho(p)(a,\mu) \cdot \mu(s'), \text{ where } p' \text{ is of the form } pa\mu s'.
\end{align*}
\]

A probabilistic execution \( Q_\rho \) induces a probability space over the sample space \( \Omega_\rho := \text{Exec}(P) \) as follows. Let \( \preceq \) denote the prefix ordering on sequences. Each \( p \in \text{Exec}^{<\omega}(P) \) generates a cone of executions: \( C_p := \{ p' \in \text{Exec}(P) \mid p \preceq p' \} \). Let \( F_P \) denote the smallest \( \sigma \)-field generated by the collection \( \{ C_p \mid p \in \text{Exec}^{<\omega}(P) \} \). There exists a unique measure \( m_\rho \) on \( F_P \) with \( m_\rho[C_p] = Q_\rho(p) \) for all \( p \) in \( \text{Exec}^{<\omega}(P) \); therefore \( Q_\rho \) gives rise to a probability space \( (\Omega_\rho, F_P, m_\rho) \).

Trace distributions are obtained from probabilistic executions by removing non-visible elements. In our case, these are states, hidden actions and distributions of states. To state this precisely, we need the notion of minimal executions: a finite execution \( p \) of \( P \) is said to be **minimal** if every proper prefix of \( p \) has a strictly shorter trace. Notice, the empty execution (i.e., the sequence containing just the initial state) is minimal. Moreover, if \( p \) is nonempty and finite, then \( p \) is minimal if and only if the last transition in \( p \) has a visible action label. For each \( a \in \text{Act}(P)^{<\omega} \), let \( \text{tr}_{\text{min}}(a) \) denote the set of minimal executions of \( P \) with trace \( a \).

Now we define a lifting of the trace operator \( \text{tr} : \text{Exec}^{<\omega}(P) \rightarrow \text{Act}(P)^{<\omega} \). Given a function \( Q : \text{Exec}^{<\omega}(P) \rightarrow [0,1] \), define \( \text{tr}(Q) : \text{Act}(P)^{<\omega} \rightarrow [0,1] \) by

\[
\text{tr}(Q)(a) := \sum_{p \in \text{tr}_{\text{min}}(a)} Q(p).
\]

Given an output scheduler \( \rho \) of a closed PIOA \( P \), the **trace distribution** induced by \( \rho \) (denoted \( D_\rho \)) is simply the result of applying \( \text{tr} \) to the probabilistic execution \( Q_\rho \). That is, \( D_\rho := \text{tr}(Q_\rho) \). We often use variables \( D, D' \), etc. for trace distributions, thus leaving the scheduler \( \rho \) implicit.

Similar to the case of probabilistic executions, each \( D_\rho \) induces a probability measure on the sample space \( \Omega := \text{Act}(P)^{<\omega} \). There the \( \sigma \)-field \( F \) is generated by the collection \( \{ C_\alpha \mid \alpha \in \text{Act}(P)^{<\omega} \} \), where \( C_\alpha := \{ \alpha' \in \Omega \mid \alpha \preceq \alpha' \} \). The measure \( m^\rho \) on \( F \) is uniquely determined by the equations \( m^\rho[C_\alpha] = D_\rho(\alpha) \) for all \( \alpha \in \text{Act}(P)^{<\omega} \).

In the literature, most authors define probabilistic executions (resp. trace distributions) to be the probability spaces \( (\Omega, F, m^\rho) \) (resp. \( (\Omega, F, m_\rho) \)). Here we find it more natural to reason with the functions \( Q_\rho \) and \( D_\rho \), rather
than the induced measures. We refer to [Seg95] for these alternative definitions and proofs that they are equivalent to our versions.

3 Switched Probabilistic I/O Automata

As we argued in Section 1, one must distinguish between global and local nondeterministic choices and must resolve them separately. This section describes our solution, namely, an explicit mechanism of control exchange among parallel components. The presentation is organized as follows: (i) first we define pre-switched automata, where we describe control action signatures and the Boolean-valued state variable active; (ii) then we introduce the notion of input well-behaved executions of a pre-switched automaton and state four axioms defining switched automata; (iii) finally, we introduce the notion of a scheduled automaton, essentially a switched automaton paired with an I/O scheduler.

For technical simplicity, we assume a universal set Act of action symbols such that Act(P) ⊆ Act for every PIOA P. Moreover, Act is partitioned into two sets: BAct (basic actions) and CAct (control actions). Both sets are assumed to be countably infinite, so we can rename hidden actions using fresh symbols whenever necessary (cf. Section 5).

**Definition 2.** A pre-switched automaton P is a PIOA endowed with a function active : States(P) → {0,1} and a set Sync ⊆ O ∩ CAct of synchronized control actions.

We use variables P, Q, etc. to denote pre-switched automata. Given a pre-switched automaton P, we further classify its action symbols:

- BI := I ∩ BAct (basic inputs);
- BO := O ∩ BAct (basic outputs);
- CI := I ∩ CAct (control inputs);
- CO := (O ∩ CAct) \ Sync (control outputs).

Essentially, we have a partition \{BI, BO, H, CI, CO, Sync\} of Act(P). We say that P is *initially active* if active(s0) = 1. Otherwise, it is *initially inactive*.

As described in Section 1, the Boolean-valued function active on the states of P indicates whether P is active or inactive, while control actions allow P to exchange control with its environment. The designation of synchronized control actions helps to achieve the “handshake” condition on control synchronizations: whenever we compose two automata, we classify the shared control actions as “synchronized”, so that they are no longer available for further synchronization with a third component. This is made precise in the definitions of compatibility and composition for pre-switched automata.

A finite set of pre-switched automata \{P_1, ..., P_n\} is said to be compatible if

(i) \{P_1, ..., P_n\} is a compatible set of PIOAs;
(ii) for all i ≠ j, Act(P_i) ∩ Sync_j = CI_i ∩ CI_j = ∅;
(iii) at most one P_i is initially active. Notice that such a set is compatible if and only if for all i ≠ j, P_i and P_j are compatible. The **parallel composition** of \{P_1, ..., P_n\}, denoted \[\bigparallel_{1≤i≤n} P_i\], is the result of composing P_1, ..., P_n as PIOAs, together with:
- \( \operatorname{Sync} := \bigcup_{1 \leq i \leq n} \operatorname{Sync}_i \cup \bigcup_{1 \leq i < j \leq n} (CI_i \cap CO_j) \);

- \( \operatorname{active}(s_1, \ldots, s_n) = 1 \) if and only if for some \( i \), \( \operatorname{active}_i(s_i) = 1 \).

Clearly, the composite \( \big|_{1\leq i\leq n} P_i \) is again a pre-switched automaton. In the binary case, we write \( P_1 \|_2 P_2 \) as shorthand for \( \big|_{1\leq i\leq 2} P_i \). Observe that \( P_1 \|_2 P_2 \cong P_2 \|_1 P_1 \); that is, composition of pre-switched automata is commutative up to isomorphism. Next we check that composition is also associative on the class of pre-switched automata.

**Lemma 1.** Let \( P_1, P_2 \) and \( P_3 \) be pre-switched automata. Assume \( P_1 \) is compatible with \( P_2 \), and \( P_1 \) is compatible with \( P_1 \|_2 P_2 \). Then \( P_2 \) is compatible with \( P_3 \), and \( P_1 \) is compatible with \( P_2 \|_2 P_3 \). Moreover, \( (P_1 \|_2 P_2) \|_2 P_3 \cong P_1 \|_2 (P_2 \|_2 P_3) \).

Recall that switched automata are intended to be composed in such a way that at most one component is active at any point of an execution. In particular, any environment automaton must also follow the rules of control exchange; that is, after activating some system component, the environment must itself become inactive. This leads to the definition of input well-behavedness. Let \( P \) be a pre-switched automaton. An input transition \( s \xrightarrow{a} s' \) is well-behaved if \( \operatorname{active}(s) = 0 \). An execution \( p \) of \( P \) is input well-behaved if all input transitions occurring in \( p \) are well-behaved. Let \( \operatorname{Exec}_{\text{wb}}(P) \) denote the set of finite, input well-behaved executions of \( P \). Moreover, we say that a state \( s \) is input well-behaved reachable, notation \( \operatorname{iwbr}(s) \), if there exists an input well-behaved execution \( p \) such that \( s = \operatorname{last}(p) \). Clearly, the empty execution is input well-behaved and thus the initial state is always input well-behaved reachable. If \( P \) is closed (i.e., \( I = \emptyset \)), then every execution of \( P \) is trivially input well-behaved and every reachable state is input well-behaved reachable. We are now prepared to define the notion of switched probabilistic I/O automata.

**Definition 3.** A switched (probabilistic I/O) automaton is a pre-switched automaton \( P \) that satisfies the following axioms.

\[
\begin{align*}
    s \xrightarrow{a} s' & \quad \operatorname{active}(s) = 0 \quad \Rightarrow \quad a \in I \quad \text{(1)} \\
    s \xrightarrow{a} s' & \quad a \in CI \\n    \quad \Rightarrow \quad \operatorname{active}(s') = 1 \quad \text{(2)} \\
    s \xrightarrow{a} s' & \quad a \notin CI \cup CO \\n    \quad \Rightarrow \quad \operatorname{active}(s) = \operatorname{active}(s') \quad \text{(3)} \\
    \operatorname{iwbr}(s) & \quad \operatorname{active}(s) \quad s \xrightarrow{a} s' \quad a \in CO \\n    \quad \Rightarrow \quad \operatorname{active}(s') = 0 \quad \text{(4)}
\end{align*}
\]

These four axioms formalize our intuitions about control passage. Axiom (1) requires all inactive states to be quiescent. Axioms (2) and (4) say that control inputs lead to active states and control outputs to inactive states. Axiom (3) says that non-control transitions and synchronized control transitions do not change the activity status. Together, they describe an “activity cycle” for the automaton \( P \): (i) while in inactive mode, \( P \) does not enable locally controlled transitions, although it may still receive inputs from its environment; (ii) when \( P \) receives a control input it moves into active mode, where it may perform hidden or output transitions, possibly followed by a control output; (iii) via this control output \( P \) returns to inactive mode.
Notice that Axiom (4) is required for input well-behaved reachable states only. Without this relaxation, the composition of two switched automata may not satisfy Axiom (4).

We proceed to confirm that the class of switched automata is closed under the parallel composition operator for pre-switched automata. A set \( \{P_1, \ldots, P_n\} \) of switched automata is compatible if the set of underlying pre-switched automata is compatible. Define the composite, \( \|_{1 \leq i \leq n} P_i \), to be the result of composing the switched automata as pre-switched automata. The first three axioms can be verified by unfolding the definition of \textit{active} in a composition and applying appropriate axioms for the components. Axiom (4) follows from Lemma 2 below. The proof is by induction on the length of executions and relies heavily on invariant-style reasoning based on the definition of input well-behaved executions and the axioms of switched automata.

**Lemma 2.** Let \( \{P_1, \ldots, P_n\} \) be a compatible set of switched automata. For each finite, input well-behaved execution \( p \) of \( \|_{1 \leq i \leq n} P_i \), we have:

(i) for all \( i \), \( \pi_i(p) \) is also input well-behaved;
(ii) there is at most one \( i \) such that \( \text{active}_i(\pi_i(\text{last}(p))) = 1 \).

To summarize, \( \|_{1 \leq i \leq n} \) is a well-defined \( n \)-ary operator for switched automata and, in the binary case, associativity of \( \| \) follows from Lemma 1.

Next we turn to scheduling decisions. The notion of I/O schedulers for switched automata is inherited from that of its underlying PIOA.

**Definition 4.** A scheduled automaton is a triple \( \langle P, \sigma, \rho \rangle \) such that \( P \) is a switched automaton and \( \langle \sigma, \rho \rangle \) is an I/O scheduler for \( P \).

We use letters \( S, T, \) etc. to denote scheduled automata. For each \( 1 \leq i \leq n \), let \( S_i \) denote a scheduled automaton \( \langle P_i, \sigma_i, \rho_i \rangle \). The set \( \{S_i \mid 1 \leq i \leq n\} \) is said to be compatible if \( \{P_i \mid 1 \leq i \leq n\} \) is compatible as a set of switched automata. Given such a compatible set of scheduled automata, we obtain its composite by combining the I/O schedulers \( \{\langle \sigma_i, \rho_i \rangle \mid 1 \leq i \leq n\} \) into an I/O scheduler \( \langle \sigma, \rho \rangle \) for the switched automaton \( \|_{1 \leq i \leq n} P_i \).

**Definition 5.** Suppose \( \{S_i \mid 1 \leq i \leq n\} \) is a compatible set of scheduled automata, where \( S_i = \langle P_i, \sigma_i, \rho_i \rangle \) for each \( i \). We construct from this set a composite scheduled automaton \( \|_{1 \leq i \leq n} S_i := \langle P, \sigma, \rho \rangle \) as follows.

\( P := \|_{1 \leq i \leq n} P_i \).

- For every finite execution \( p \) of \( P \) with \( \text{last}(p) = s \) and for every \( a \in I \),
  - \( \sigma(p, a) \rightarrow b \mu \) := 0 if \( t \neq s \) or \( b \neq a \);
  - otherwise, \( \sigma(p, a) \rightarrow s \mu_0 \times \cdots \times \mu_n \) := \( \Pi_i c_i \) where \( c_i \) equals
    * \( \sigma_i(p, a)(\mu_i) \), if \( a \in I_i \);
    * 1, otherwise.

- For every finite execution \( p \) of \( P \) with \( \text{last}(p) = s \),
  - \( \rho(p) \rightarrow a \mu \) := 0 if \( p \) is not input well-behaved, \( t \neq s \), or \( a \not\in L \);
• otherwise, $\rho(p)(s \xrightarrow{a_0} \mu_0 \times \ldots \times \mu_n) := \Pi_i c_i$, where $c_i$ equals

* $\rho_i(\pi_i(p))(a_0, \mu_0)$, if $a \in L_i$;
* $\sigma_i(\pi_i(p), a)(\mu_i)$ if $a \in I_i$;
* 1, otherwise.

It is routine to check that $\sigma(p, a)$ is a discrete distribution for all $p \in \text{Exec}^{\leq w}(P)$ and $a \in I$. Lemma 2 guarantees that, at the end of every input well-behaved finite execution $p$, there is at most one $i$ such that component $i$ enables a locally controlled transition. This allows us to conclude that $\rho(p)$ is a discrete sub-distribution for all $p \in \text{Exec}^{\leq w}(P)$.

As usual, we write $S_1 | S_2$ for $\| \leq i \leq 2 S_i$, provided $S_1$ and $S_2$ are compatible. Associativity of $\|$ for scheduled automata follows from that for switched automata and a routine check on the I/O schedulers. Finally, the notions of probabilistic executions and trace distributions for closed scheduled automata are inherited from those of PIOAs. In particular, we write $QS$ (respectively, $DS$) for the probabilistic execution (respectively, trace distribution) induced by the output scheduler of a closed scheduled automaton $S$.

4 Projection and Pasting

In this section, we study projection and pasting of probabilistic behaviors. Such results are essential elements in constructing a compositional modeling framework. We begin by introducing the notion of regular executions, which will be used to define pseudo trace distributions for automata with open inputs. In Lemma 5, we prove that the pseudo distribution of a composite is uniquely determined by those of its components. Finally, we prove the main pasting lemma for closed automata (Lemma 7), which plays a crucial role in the proof of our main compositionality theorem (Theorem 1).

Given an execution $p$ of a switched automaton $P$, we say that $p$ is regular if it is both minimal and input well-behaved. Given a finite sequence $\alpha$ of visible actions in $P$, let $\text{tr}_{\text{reg}}(\alpha)$ denote the set of regular executions of $P$ with trace $\alpha$. Notice that regularity coincides with minimality in case $P$ is closed.

Lemma 3 states that, given a fixed trace, there is a bijective correspondence between the set of regular executions of the composite and the Cartesian product of the sets of regular executions of the two components.

**Lemma 3.** Let $X$ denote $\text{tr}_{\text{reg}}(\alpha)$ in $P_1 | P_2$. Let $Y$ and $Z$ denote $\text{tr}_{\text{reg}}(\pi_1(\alpha))$ in $P_1$ and $\text{tr}_{\text{reg}}(\pi_2(\alpha))$ in $P_2$, respectively. There exists an isomorphism $\text{zip} : Y \times Z \rightarrow X$ whose inverse is $(\pi_1, \pi_2)$.

Next we introduce a notion of pseudo probabilistic execution for automata with open inputs. The definition itself is completely analogous to probabilistic executions for closed automata; however, a pseudo probabilistic execution does not always induce a probability measure, because it does not take into account the probabilities with which inputs are provided by the environment.
Definition 6. Let $S = (P, \sigma, \rho)$ be a scheduled automaton. Define the pseudo probabilistic execution $Q$ of $S$ as follows: for all finite executions $p'$ of $S$,

- if $p'$ is of the form $s^0$, where $s^0$ is the initial state of $S$, then $Q(p') := 1$;
- if $p'$ is of the form $pa^s'$ with $a \in I$, then $Q(p') := Q(p) \cdot \sigma(p, a)(\mu) \cdot \mu(s')$;
- if $p'$ is of the form $pa^s'$ with $a \in L$, then $Q(p') := Q(p) \cdot \rho(p)(a, \mu) \cdot \mu(s')$.

Similarly, we define pseudo trace distributions.

Definition 7. Let $S = (P, \sigma, \rho)$ be a scheduled automaton. The pseudo trace distribution $D$ of $S$ is the function from $(\text{Act}(S) \setminus H_S)^\omega$ to $[0, 1]$ given by

$$D(\alpha) := \sum_{p \in \text{tr}^{-1}_H(\alpha)} Q(p),$$

where $Q$ is the pseudo probabilistic execution of $S$.

Notice that, if $S$ is closed, then the pseudo probabilistic execution of $S$ coincides with the probabilistic execution of $S$. Moreover, an execution of a closed automaton $S$ is regular if and only if it is minimal, thus the pseudo trace distribution of $S$ coincides with the trace distribution of $S$.

For the rest of this section, let $S$ and $T$ be a pair of compatible scheduled automata. Let $Q_{S \parallel T}$, $Q_S$ and $Q_T$ denote the pseudo probabilistic executions of $S \parallel T$, $S$ and $T$, respectively. Similarly for pseudo trace distributions $D_{S \parallel T}$, $D_S$ and $D_T$. Lemma 4 below says we can project a pseudo probabilistic execution of the composite to yield pseudo probabilistic executions of the components. The proof is routine, by induction on the length of executions. Lemma 5 then combines Lemma 3 and Lemma 4 to show the analogous projection result for pseudo trace distributions.

Lemma 4. For all finite executions $p$ of $S \parallel T$, we have $Q_{S \parallel T}(p) = Q_S(\pi_1(p)) \cdot Q_T(\pi_2(p))$.

Lemma 5. Let $\alpha$ be a finite sequence of visible action symbols of $S \parallel T$. Then $D_{S \parallel T}(\alpha) = D_S(\pi_1(\alpha)) \cdot D_T(\pi_2(\alpha))$.

To prove the main pasting lemma, we need yet another technical result; namely, inputs must be received with probability 1. This can be viewed as “input enabling” in the probabilistic sense and it follows from basic properties of target distributions and input schedulers.

Lemma 6. Let $\alpha$ be a finite sequence of visible action symbols of $S \parallel T$ and let $a \in \text{Act}(S \parallel T)$ be given. If $a$ is not locally controlled by $T$, then $D_T(\pi_2(\alpha)) = D_T(\pi_2(\alpha a))$.

Two switched/scheduled automata are said to be comparable if they have the same visible signature and their start states have the same status. We are now ready for the main pasting lemma.

Lemma 7 (Pasting). Let $S_1$, $S_2$, $T_1$ and $T_2$ be scheduled automata satisfying (i) $S_1$ and $S_2$ are comparable; (ii) $\{S_1, T_1\}$, $\{S_2, T_2\}$ and $\{S_1, T_2\}$ are compatible sets; (iii) the pseudo trace distributions $D_{S_1 \parallel T_1}$ and $D_{S_2 \parallel T_2}$ coincide (denoted $D$). Then $D$ also coincides with the pseudo trace distribution $D_{S_1 \parallel T_2}$. 
5 Renaming and Hiding

In this section, we consider the standard renaming and hiding operators. We start with an equivalence relation on switched automata: $P_1 \equiv_R P_2$ just in case there exists a bijection $f : H_1 \rightarrow H_2$ such that $P_2$ can be obtained from $P_1$ by replacing every hidden action symbol $a \in H_1$ by $f(a) \in H_2$ (notation: $P_2 = f(P_1)$).

It is routine to check this is in fact an equivalence relation. If $P_1 \equiv_R P_2$, we say that $P_2$ can be obtained from $P_1$ by renaming of hidden actions. This operation also induces an equivalence relation on scheduled automata: $(P_1, a_1, p_1) \equiv_R (P_2, a_2, p_2)$ just in case there exists a renaming function $f$ such that $P_1 \equiv_R P_2$ via $f$ and $(a_2, p_2)$ is obtained from $(a_1, p_1)$ via $f$ and $f^{-1}$ (notation: $S_2 = f(S_1)$).

The following lemma says, as long as the renaming operation does not introduce incompatibility of signatures, it does not affect the behavior of an automaton in a closing context.

**Lemma 8.** Let $S$ and $C$ be compatible scheduled automata with $S || C$ closed. Suppose $S \equiv_R S'$ via the renaming function $f : H \rightarrow H'$ with $H'$ disjoint from $\text{Act}(C)$. Then $(S', C)$ is closed and compatible and $DS || C = DS' || C$.

Next we consider the issue of hiding output actions. Let $\text{Hide}$ denote the standard hiding operator for PIOA. This is also an operator for switched automata, provided we hide only basic outputs and synchronized control actions.

**Lemma 9.** Let $P$ be a switched automaton and let $\Omega \subseteq BO \cup \text{Sync}$ be given. Then $\text{Hide}_\Omega(P)$ is again a switched automaton.

Notice that every I/O scheduler for $P$ is an I/O scheduler for $\text{Hide}_\Omega(P)$. Therefore $\text{Hide}$ can be extended to scheduled automata:

$$\text{Hide}_\Omega(P, \sigma, \rho) := (\text{Hide}_\Omega(P), \sigma, \rho).$$

In the rest of this section we investigate the effect of $\text{Hide}_\Omega$ on (pseudo) trace distributions. Let $S = (P, \sigma, \rho)$ be a scheduled automaton with signature $(I, O, H)$. For convenience, write $P'$ for $\text{Hide}_\Omega(P)$, $O'$ for $O \setminus \Omega$, and $\text{tr}'$ for the trace operator for $\text{Hide}_\Omega(P)$. (If we view $\text{Hide}_\Omega$ as an operator on traces, then $\text{tr}'$ is precisely $\text{Hide}_\Omega \circ \text{tr}$.)

Moreover, for all $\beta' \in (I \cup O')^\omega$, let $\mathcal{M}_{\beta'}$ denote the set of all minimal (w.r.t. $\square$) traces in $\text{Hide}_\Omega^{-1}(\beta')$. That is, if $\beta'$ is empty, then $\mathcal{M}_{\beta'}$ is the singleton set containing the empty trace $\varepsilon$; otherwise,

$$\mathcal{M}_{\beta'} := \{ \beta \in (I \cup O)^\omega \mid \text{Hide}_\Omega(\beta) = \beta' \text{ and the last symbol on } \beta \text{ is not in } \Omega \}.$$  

We make a simple observation about minimal executions of $P$ and those of $P'$.

**Lemma 10.** For all $\beta' \in (I \cup O')^\omega$, the following two sets are equal:

- $X := \bigcup_{\beta \in \mathcal{M}_{\beta'}} \{ p \in \text{Exec}^\omega(P) \mid \text{tr}(p) = \beta \text{ and } p \text{ minimal w.r.t. tr} \}$;
Now consider the pseudo trace distribution $D_S$. Define the effect of $\text{Hide}_\Omega$ on $D_S$ to be the following function from $O'^{<\omega}$ to $[0,1]$:

$$\text{Hide}_\Omega(D_S)(\beta') := \sum_{\beta \in \mathcal{M}_{\beta'}} D_S(\beta).$$

We have the following corollary of Lemma 10.

**Corollary 1.** The pseudo trace distribution of $\text{Hide}_\Omega(S)$ is precisely $\text{Hide}_\Omega(D_S)$. That is, $D_{\text{Hide}_\Omega(S)} = \text{Hide}_\Omega(D_S)$.

Finally, we consider the effect of hiding in a parallel composition. We claim that the act of hiding in one component does not affect the behavior of the other, as long as the actions being hidden in the first component are not observable by the second component. This idea is captured in the following lemma, which follows from Corollary 1 and Lemma 5.

**Lemma 11.** Let $S_1, S_2, T$ be scheduled automata satisfying: (i) $S_1$ and $S_2$ are comparable and (ii) $T$ is compatible with $S_1$ and $S_2$. Let $\Omega \subseteq O_T$ be given and let $T'$ denote $\text{Hide}_\Omega(T)$. If $T'$ is compatible with $S_1$ (and thus with $S_2$), then

$$D_{S_1 | T} = D_{S_2 | T} \iff D_{S_1 | T'} = D_{S_2 | T'}.$$

## 6 Probabilistic Systems

In this section, we give a formal definition of our implementation preorder and prove compositionality. The basic approach is to model a system as a switched PIOA together with a set of I/O schedulers. Observable behavior is then defined in terms of trace distributions induced by the prescribed schedulers.

Formally, a **probabilistic system** $P$ is a set of scheduled automata that share a common underlying switched automaton. (Equivalently, a probabilistic system is a pair $(P, S)$ where $P$ is a switched automaton and $S$ is a set of I/O schedulers for $P$.) Such a system is **full** if $S$ is the set of all possible I/O schedulers for $P$.

Two probabilistic systems $P_1 = (P_1, S_1)$ and $P_2 = (P_2, S_2)$ are **compatible** just in case $P_1$ is compatible with $P_2$. The **parallel composite** of $P_1$ and $P_2$, denoted $P_1 \parallel P_2$, is the probabilistic system: $\{S_1 | S_2 | S_1 \in P_1 \text{ and } S_2 \in P_2\}$. Notice the underlying automaton of the composite is $P_1 | P_2$.

Let $S$ be a scheduled automaton. A **context** for $S$ is a scheduled automaton $C$ such that (i) $C$ is compatible with $S$; (ii) $S$ and $C$ have complementary signatures (i.e., $I_C = O_S$ and $I_S = O_C$). Given probabilistic system $P = (P, S)$, we say that $D$ is a **trace distribution** of $P$ just in case there exist scheduled automata $S \in P$ and context $C$ for $S$ such that $D = D_{S|C}$. We write $\text{td}(P)$ for the set of trace distributions of $P$.

Two probabilistic systems are **comparable** whenever the underlying switched automata are comparable. Given comparable systems $P_1$ and $P_2$, we define the
trace distribution preorder by: \( P_1 \leq_{td} P_2 \) whenever \( td(P_1) \subseteq td(P_2) \). We are now ready to present our main theorem, namely, that the trace distribution preorder for probabilistic systems is compositional.

**Theorem 1.** Let \( P_1 \) and \( P_2 \) be comparable probabilistic systems with \( P_1 \leq_{td} P_2 \). Suppose \( P_3 \) is compatible with both \( P_1 \) and \( P_2 \). Then \( P_1 \| P_3 \leq_{td} P_2 \| P_3 \).

### 7 PIOA Revisited

Before concluding, we give an example in which switched automata are used to obtain a new trace-based semantics for general PIOAs. The idea is to convert a general PIOA to a switched PIOA by adding control actions and activity classification. We then hide all control actions in trace distributions generated by the resulting switched PIOA. In many cases, this yields a set of trace distributions strictly smaller than that considered by Segala [Seg95].

Let \( P \) be a PIOA and assume \( Act(P) \subseteq BAct \). Let \( go, done \in CAct \) be fresh symbols and let \( b_0 \) be a Boolean value. The switch extension of \( P \) with \( go, done \) and initialization \( b_0 \) (notation: \( E(P, go, done, b_0) \)), is the switched automaton \( P' \) constructed as follows:

- States\((P') = States(P) \times \{0, 1\} \) and the start state of \( P' \) is \( \langle s^0, b_0 \rangle \);
- \( I' = I \cup \{go\}, O' = O \cup \{done\}, \) and \( Sync' = \emptyset \);
- active\(', ((s, b)) = b \) for \( b \in \{0, 1\} \);
- the transition relation is the union of the following:
  - \( \langle (s, 1), a, \mu^1 \rangle \) if \( s \xrightarrow{a} \mu \) in \( P \),
  - \( \langle (s, 0), a, \mu^0 \rangle \) if \( s \xrightarrow{a} \mu \) in \( P \) and \( a \in I \),
  - \( \langle (s, b), go, ((s, 1) \rightarrow 1) \rangle \) if \( s \in States(P) \) and \( b \in \{0, 1\} \),
  - \( \langle (s, 1), done, ((s, 0) \rightarrow 1) \rangle \) if \( s \in States(P) \),

where \( \mu^b \) denotes the distribution that assigns probability \( \mu(t) \) to \( \langle t, b \rangle \) and 0 to \( \langle t, 1 - b \rangle \).

Roughly speaking, \( P' \) is obtained from \( P \) by (i) adding a Boolean flag active\'} to each state; (ii) enabling locally controlled transitions only if active\'} = 1; and (iii) adding go and done transitions which update active\'} appropriately. It is not hard to check that \( P' \) satisfies all axioms of switched automata. Moreover, the pair (go, done) can be easily generalized to a pair of disjoint sets of control actions.

Given any two compatible PIOAs, we can always extend them with complementary control actions and initialization statuses, resulting in a pair of compatible switched automata. As an example, we consider the automata Late and Toss in Figure 1. Actions \( a, b \) and \( c \) are considered outputs of Late, whereas action \( a \) is an input of Toss and actions \( e \) and \( f \) are outputs of Toss. The following diagrams illustrate \( E(Late, go, done, 1) \) and \( E(Toss, done, go, 0) \). For a clearer picture, we have omitted the probabilities on the input a-transition in Toss, as well as all nonessential input loops. The active region, which is identical to the
original PIOA, is drawn in the foreground. The inactive region, in which all locally controlled transitions are removed, is in the background. Each two-headed arrow indicates a control output from active to inactive and a control input from inactive to active.

Now consider the problematic trace distribution $D_0$ of $\text{Late} \parallel \text{Toss}$, as described in Section 1. Let $P_1$ and $P_2$ denote the full probabilistic systems on $\mathcal{E}(\text{Late}, \text{go}, \text{done}, 1)$ and $\mathcal{E}(\text{Toss}, \text{done}, \text{go}, 0)$, respectively. As we compose these two systems, $D_0$ is no longer a trace distribution of $P_1 || P_2$ (even after hiding $\text{go}$ and $\text{done}$), because I/O schedulers in $P_1$ have no way of knowing whether action $d$ or action $e$ was performed by $P_2$, thus they cannot establish the correlations between actions $d$ and $b$, and between actions $e$ and $c$.

This leads to our proposal of a new notion of visible behaviors for PIOA. Let $P$ be a PIOA and let $P$ be the full probabilistic system on $\mathcal{E}(P, \text{go}, \text{done}, 0)$. A PIOA $E$ is a context for $P$ if $\mathcal{O}(P) = \mathcal{O}_P$, $O_E = \mathcal{I}_P$, and $E$ is compatible with $P$. For each such $E$, write $P_E$ for the full probabilistic system on $\mathcal{E}(E, \text{done}, \text{go}, 1)$. We say that $D$ is a trace distribution of $P$ if there exists a context $E$ for $P$ such that $D \in \text{td} (\text{Hide}_{\{\text{go}, \text{done}\}}(P || P_E))$, where $\text{Hide}$ is lifted from scheduled automata to probabilistic systems.

8 Conclusions and Further Work

Our ultimate goal, of course, is to obtain a compositional semantics for PIOAs. The notion of switch extensions opens up an array of new options for that end. A promising approach is to model each system as a finite set of PIOAs, rather than a single PIOA. Composition is taken to be set union, representing the act of placing two sets of processes in the same computing environment. Behavior is then defined in terms of switch extensions, which instantiate the system with a particular network topology for control passage. In that case, a behavior of a finite set $\mathcal{F}$ is determined by (i) a context $E$ for $\mathcal{F}$; (ii) a combination of switch extensions of $\mathcal{F} \cup \{E\}$; (iii) a combination of I/O schedulers for these switch extensions. By ranging over all contexts and all extension-scheduler combinations, we capture all possible behaviors of the system represented by $\mathcal{F}$.

In other future work, we plan to apply our theory of composition for PIOAs to the task of verifying security protocols. For example, we will try to model typical Oblivious Transfer protocols within the PIOA framework and verify correctness in the style of Canetti's Universal Composability [Can01]. We will also explore the use of PIOAs as a semantic model for the probabilistic polynomial time process calculus of Lincoln, Mitchell, Mitchell and Scedrov [LMMS98].
References


