Testing and Validating the Quality of Specifications

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Abstract

Model-based testing of state based systems is known to be able to spot non-conformance issues. However, up to half of these issues appear to be errors in the model rather than in the system under test. Errors in the specification at least hamper the prompt delivery of the software, so it is worthwhile to invest in the quality of the specification. Worse, errors in the specification that are also present in the system under test cannot be detected by model-based testing. In this paper we show how very desirable properties of specifications can be checked by systematic automated testing of the specifications themselves. We show how useful properties of specifications can be found by generalization of incorrect transitions encountered in simulation of the model.

1. Introduction

Software systems tend to become larger and more complex. The requirements for the system are changing rapidly, often during the construction of the system. Finally, the time to market should be reduced. These factors can easily hamper the quality of the product. Systematic testing is by far the most used and effective way to determine the quality of a software system. In model-based testing, MBT, the test cases are generated on the fly from a formal specification. This ensures that the test suite is always up-to-date with that specification. More testing for improved confidence in the quality of the software is achieved by just changing a parameter in the test system.

Experience shows that writing a formal specification is an useful activity on its own. Many inaccuracies and misconceptions in the informal or semiformal specification/design of the system are found during the creation of such a formal specification. Testing real world systems also reveals that a significant fraction of the issues found during testing (on average 25%) is due to issues in the formal specification rather than the system under test (sut). Since a specification is a formal artefact similar in nature to the actual software, this is not astonishing. When a specification differs from a sut the issue is spotted during the tests and can be corrected. Nevertheless, the analysis and correction of issues in the specification is time-consuming and hence delays the release of the sut. It is therefore desirable to have high quality specifications before testing the sut is started.

In our model-based test system G\textsuperscript{\forall}st [12, 13], we use a transition function in the high level functional programming language \texttt{Clean} [15] as specification. The reasons to use a functional programming language as carrier of our models are:

- Functional languages exclude side-effects. This implies pure and clear semantics.
- Functions appear to be concise models. As shown in this paper they can be easily composed and transformed.
- \texttt{Clean} offers a complete set of high level programming primitives and very useful libraries. The rich type system, particularly the tailor made (recursive) data types, enables us to write very clear and expressive models. Parameterized types are especially useful if we model an extended state machine (a model with an unbounded number of states, inputs or outputs). By using an existing language all these things are obtained for free.
- The static type system of \texttt{Clean} will check that all identifiers that are used in the specification are defined and that they are used in a type correct way.
- The generic programming facilities (see appendix for a short introduction) of \texttt{Clean} enable us to define operations like equality, printing and generation of data elements that are needed in each test once and for all. This implies that the tester can derive these operations automatically for tailor made types in its test rather defining them manually. If the test engineer has specific wishes she can, of course, define her own algorithms instead of using the generic versions.
• Clean is a very efficient language, both as implementation and for the generated code. This implies that the execution of tests is efficient, and that the turn around time after changes in the model or testing parameters is extremely short.

Before we can start testing, the Clean-system checks the specification for matters like type correctness and whether all used identifiers are properly defined. This definitely contributes to the quality of the specifications used for testing. Still there can be many things wrong with a specification. A specification can prescribe unintended behavior in a formally completely correct way. These kind of issues cannot be detected automatically and have to be found by human inspection, perhaps supported by simulation of the execution based on the model. But the specification can also contain errors of a more technical nature which are trackable by a technical approach. For instance, a state based specification of the system can be partial while the specification is assumed to be total, or it can be nondeterministic while it is assumed to be deterministic. Other potential problems are that some transitions violate certain (domain specific) constraints.

In this paper we show that many of these technical issues in the specifications can be found by systematic testing of the specifications themselves. When the properties and invariants of the specification are stated explicitly, the logical based branch of our test tool is able to test these properties of the specification independent from any implementation, even before an implementation exists. The advantage of this approach over model checking is that we can stay within the formalism, no transformation of models is needed.

In order to make this paper self contained we introduce the specification and testing of reactive systems in section 2. In section 3 we illustrate the kind of specifications used by two examples. They will be used in the rest of this paper as running examples. Section 4 shows that some properties relating input, output and state is not feasible. We specify reactive systems by an extended state machine and require that the observed behavior of the system conforms to this specification.

An Extended State Machine, ESM, as used by Gvst consists of states with labeled transitions between them. A transition is of the form $s \xrightarrow{i/o} t$, where $s$, $t$ are states, $i$ is an input which triggers the transition, and $o$ is a, possibly empty, list of outputs. A transition $s \xrightarrow{i/o} t$ is formalized as a tuple $(s, i, o, t)$. The set $\delta_s$ contains all allowed transitions in the specification. The transition function is defined by $\delta_f(s, i) = \{ (o, t) | (s, i, o, t) \in \delta_s \}$. The type of this function is: $S \times I \rightarrow \mathcal{P}(O \times S)$ where $S$ is the type of states, $I$ is the type of inputs, and $O$ is the type of outputs. We use $\mathcal{P}X$ as notation for a set of elements of type $X$.

In order to obtain a compact representation it is often more convenient to use functions from output sequences to states as result of $\delta_f$, rather than enumerating all possible tuples. For a single transition we have: $s \xrightarrow{i/o} t \iff \exists f \in \delta_f(s, i) \land t \in f(o)$. In this way, a single function can represent arbitrary many output target-state tuples.

A specification is total or complete if for every $s \in S$ and $i \in I$ there is at least one output and target state defined. Specifications are partial if they are not total. Hence, a specification is partial if for some state $s$ and input $i$, $\delta_f(s, i) = \emptyset$. If a specification is nondeterministic there are $s \in S$ and $i \in I$ with more than one associated target state. A specification is deterministic if for all states and inputs the size of the set of targets contains at most one element: $\# \delta_f(s, i) \leq 1$.

A trace $\sigma$ is a sequence of inputs and associated outputs from the given state. A trace is defined inductively: the empty trace connects a state to itself: $s \xrightarrow{\epsilon} s$. We can combine a trace $s \xrightarrow{\sigma} t$ and a transition $t \xrightarrow{i/o} u$, to the trace $s \xrightarrow{\sigma i/o} u$. An input trace contains only the input elements of a trace.

We define $s \xrightarrow{i/o} \equiv \exists t. s \xrightarrow{i/o} t$ and $s \xrightarrow{\sigma} \equiv \exists t. s \xrightarrow{\sigma} t$, to omit target states if we do not need them. All traces from some state $s$ are defined as: $\text{traces}(s) \equiv \{ \sigma | s \xrightarrow{\sigma} \}$. The set of inputs allowed in some state is given by $\text{init}(s) \equiv \{ i | \exists o. s \xrightarrow{i/o} \}$. The states after applying trace
in state \( s \) are given by \( s \) after \( \sigma \equiv \{ t | s \xrightarrow{\sigma} t \} \). We overload traces, \( \text{init} \), and after for sets of states instead of a single state by taking the union of the notion for the members of the set. When the transition function, \( \delta_f \), to be used in these operations is not clear from the context, we add it as subscript.

2.1. Conformance

This section defines the conformance relation between the specification and the \( \text{sut} \). In this paper we treat terms model and specification as synonyms. It is not required that the \( \text{sut} \) and the specification have exactly the same behavior. On parts that are not covered by the specification, any behavior of the \( \text{sut} \) is allowed. On the other hand, it is not necessary that the \( \text{sut} \) shows all allowed traces for nondeterministic specifications.

The \( \text{sut} \) is modeled as a black box transition system. One can observe its traces, but not its state. The \( \text{sut} \), and its specification need not have identical input output behavior in all situations to say that the \( \text{sut} \) conforms to the specification.

The conformance of a system, \( \text{sut} \), to its specification, \( \text{spec} \), is defined as:

\[
\text{sut} \ 	ext{conf} \ 	ext{spec} \equiv \forall \sigma \in \text{traces}_{\text{spec}}(s_0),
\forall i \in \text{init}(s_0 \text{ after}_{\text{spec}} \sigma), \forall o \in O
\]

\[
(t_0 \text{ after}_{\text{sut}} \sigma) \xrightarrow{i/o} (s_0 \text{ after}_{\text{spec}} \sigma) \xrightarrow{i/o},
\]

Here \( s_0 \) is the initial state of \( \text{spec} \), and \( t_0 \) the initial state of \( \text{sut} \). Intuitively the conformance relation reads: if the specification allows input \( i \) after trace \( \sigma \), then the observed output of the \( \text{sut} \) should be allowed by the specification. The initial state \( t_0 \) of the \( \text{sut} \) is generally not known. The \( \text{sut} \) is assumed to be in state \( t_0 \) when we switch it on and when we reset it during testing.

The specification \( \text{spec} \) can be partial: nothing is specified about the behavior for some state and input combinations. The conformance relation allows any behavior of the \( \text{sut} \) if nothing is specified. Since everything is allowed, it makes no sense to test this. The \( \text{sut} \) cannot refuse inputs: in every state the \( \text{sut} \) should accept any input.

2.2. Testing conformance

The conformance relation covers all possible traces. However, most extended state machines can show an infinite number of traces and each of these traces can have an unbounded length. Hence, it is generally impossible to determine conformance by investigating all traces completely. A test system approximates conformance by checking a finite number of finite traces. \( \text{Gvst} \) tests an initial part (by default 1000 steps) of a finite number (by default 100) of traces. The test system records that set of last states of the traces, rather than the complete traces. The actual trace is only recorded to give information about a conformance problem if it is detected. \( \text{Gvst} \) is able to generate inputs, also for complex user defined recursive data types, fully automatically. However, the user can guide this generation completely if that would be desired. The details are not relevant for this paper. See [13, 11].

2.3. Representation of specifications in \( \text{Gvst} \)

As shown above two types of specification functions are used: \( S \times I \rightarrow \mathcal{P}(O \times S) \) and \( S \times I \rightarrow \mathcal{P}(O \rightarrow S) \). These specification functions are represented in \( \text{Gvst} \) by functions in the functional programming language \( \text{Clean} \). The type of all specifications is given by the type synonym \( \text{spec} \). We use type variables \( (s, i, o) \) to abstract from concrete types for state \( (S) \), input \( (I) \) and output \( (O) \). This guarantees that the test tool \( \text{Gvst} \) is able to test specifications of any type for state, input and output. The main test functions only impose some restrictions on these types. These restrictions guarantee for instance that these types can be printed (in order to generate traces) and that there is an equivalence relation to compare output elements. The algebraic data type \( \text{Trans} \) captures the fact that a specification yields a set of tuples or functions when it is given the current state and input. In the \( \text{Clean} \) representation of the transition functions we use lists of outputs, \( [o] \), rather than single output elements, \( o \), for two good reasons. First, this gives a compact and convenient notation for no output. Without the list we either had to extend each output type with an element indicating that there is no output, or we had to use a type like \( \text{Maybe} O \) which lifts the domain \( o \) with a \( \text{Nothing} \) value in case of no output, and \( \text{Just} O \) values otherwise. Second, in contrast to the \( \text{Maybe} \) type, the lists allow sequences of outputs which is often handy.

The type synonym definition for \( \text{spec} \) allows us to use terms like \( \text{Spec State In Out} \) instead of the actual function type \( S \rightarrow I \rightarrow [\text{Trans Out State}] \). The algebraic data type \( \text{Trans} \) is motivated above.

\[
: : \text{Spec s i o} \Longrightarrow s \rightarrow i \rightarrow [\text{Trans o s}]
\\:: \text{Trans o s} =\text{ Pt} [o] s \mid \text{Pt} ([o] \rightarrow [s])
\]

We give some examples in section 3 below.

3. Examples

In this section we introduce two examples. These examples illustrate the kind of specifications used by \( \text{Gvst} \) and will be used in this paper to illustrate how these specifications can be transformed to give the desired properties and we show how properties of these specifications can be tested.

The first example consists of some variants of a vending machine that occurs in many papers about MBT. The second example is a larger and more realistic specification that
models a system giving information about telephone numbers.

3.1. Example 1: vending machines

Figure 1 shows two extended state machines modeling vending machines. The global specification of these vending machines is that it can deliver tea or coffee after insertion of coins with a sufficient value, and pressing the correct button.

![Figure 1. The vending machine on the left delivers tea, the right one produces coffee.](image)

An input is either a nickel, a dime, or pressing the tea or coffee button. The output is either tea or coffee. This is modeled by tailor-made algebraic data types. The state of the machine is recorded in the algebraic data type State, which records the amount of money inserted as an integer.

```plaintext
:: In = Nickel | Dime | ButC | ButT // vending input
:: Out = Coffee | Tea // vending output
:: State = $ Int // vending state
```

The specification of both vending machines in G\textit{\`{e}}st is:

```plaintext
specC ::= State In \to \{ Trans Out State \} // type of modeling function
specC (S n) Dime = [Pt [] (S (n+10))] \ // insert dime
specC (S n) ButC
| n \geq 10 = [Pt [Coffee] (S (n-10))] \ // produce coffee if sufficient balance
| = [Pt [Tea] (S n)] \ // do nothing if balance is insufficient
specC s i = [] \ // otherwise no transition defined
specC ::= State In \to \{ Trans Out State \}
specC (S n) Nickel = [Pt [] (S (n+5))] \ // insert nickel
specC (S n) ButT
| n \geq 5 = [Pt [Tea] (S (n-5))] \ // produce tea if sufficient balance
| = [Pt [Tee] (S n)] \ // do nothing if balance is insufficient
specC s i = [] \ // otherwise no transition defined
```

The last line (e.g. `specC s i = []`) of both specifications indicates that nothing is specified for other combinations of state and input. If nothing is specified for a combination of state and input any behavior is allowed, see section 2.1. The existence of the alternative `[Pt [] (S n)]` for `(S n) ButC` that is chosen on insufficient balance is important. Without that alternative anything is allowed according to the conformance relation, including the delivery of the required product. By design both specifications are partial, e.g. `specC` does not specify what ought to happen on the inputs Nickel and ButT.

3.2. Example 2: Qui-Donc

This example is adapted from [16]. Qui-Donc (French for who there?) is a service of France Telecom that finds the person associated to a telephone number. We model the behavior of the Qui-Donc system by an ESM.

![Figure 2. The extended state diagram of the Qui-Donc system.](image)

Initially the system is in the Wait state. When the user dials the number of this service, the user receives a Welcome message and the system goes to the Stars state. If the user does nothing for six seconds the system gets a TimeOut event. After the third TimeOut the system returns to the Wait state with NotAllowed message. After another TimeOut the system repeats the Welcome message. After entering a Star the system is in the Digits state and produces an Enter message. In this state the user can enter a maximum of 10 digits and a Hash (#). If the user waits too long the system receives a TimeOut and repeats the Enter message. After three time outs the call is terminated with a Bye message. If the user enters an emergency number terminated with a Hash the system gives an explanation and waits for a Star. After the Star the user can do a new search. For a known 10-digit number the system gives information about the owner of that number. For other numbers the system gives an Error message and waits for a new number. In any state the user can Hangup, and the Qui-Donc system returns to its Wait state. The ESM depicted in figure 2 is specified with G\textit{\`{e}}st in figure 3. A telephone Number is a type synonym for a list of integers. The timeout count, Tcount, is an integer. The specification uses parameterized algebraic data types for the state of the system, its input and output:

```plaintext
:: Number = [Int]
:: Tcount = Int
:: Qs = Wait | Stars Tcount | Digits Number Tcount
| Info (Maybe Person) Tcount | Emergency
:: Qd = Dial | Star | Hash | Dial Int | TimeOut | Hangup
:: Qo = Welcome | NotAllowed | Enter | Error | EmInfo | Sorry
| QsString String | Help | Addr Number | Bye | InfoText
```
The appropriate instance of the operator $+$ it is even possible to add specifications with an expression like $\text{set}$. The specification obtained by $\text{set}$ contains the transitions in $s$ as well as the transitions in $t$.

\[ \text{instance } s \rightarrow (+ a b) \quad \text{where } (+) x y z = \lambda x.f x + g x \]
\[ \text{instance } a \rightarrow (s = s + s) \]

Using this operator we can define a vending machine $\text{specV}$ that combines the behavior of $\text{specS}$ and $\text{specC}$, or a vending machine $\text{specC2}$ that allows the user to pay coffee with two nickels as:

\[ \text{specS :: Spec State In Out} \]
\[ \text{specC :: Spec State In Out} \]
\[ \text{specC2 :: Spec State In Out} \]
\[ \text{specV = specC + specC2} \]
\[ \text{specC} \]
\[ \text{specC2} \]
\[ \text{specV} \]

The machine modeled by $\text{specV}$ is able to produce coffee as well as tea. Note that $\text{specC}$ also allows the user to pay coffee with two nickels and is able to produce two teas for a dime. Even if the user restricts herself to tea, the behavior of $\text{specV}$ is different (allows paying with dimes) from $\text{specC}$. The combined system $\text{specV}$ has the same type of states ($\text{states}$) as its components $\text{specC}$ and $\text{specS}$, this is exactly what we want. In the traditional composition of FSMs [17], the new state is usually the product of the old states, here this would be ($\text{State, State}$). We can also define a composition of models that tuples the state of the existing machines, if that would be required.

In a similar way one can define the difference of specifications, that is all behavior of one model that is not shown by another model. As defined above, a specification is total if it contains at least one transition for each state and input. The default way to make a specification total is to add a transition with at least one transition for each state and input. The default way to make a specification total is to add a transition with at least one transition for each state and input. The default way to make a specification total is to add a transition with at least one transition for each state and input. The default way to make a specification total is to add a transition with at least one transition for each state and input.

As defined above, a specification is total if it contains at least one transition for each state and input. The default way to make a specification total is to add a transition with an empty output sequence that preserves the state if nothing else is defined. Using a higher order function it is easy to define a function that makes any given specification total:

\[ \text{mkTotal :: } \text{Spec s i o} \rightarrow \text{Spec s i o} \]
\[ \text{mkTotal spec} = \lambda i.\text{case spec s i of} \]
\[ \text{[i] } \rightarrow \text{[i] s} \]
\[ \text{t } = \text{t} \]

It might be tempting to define this operation to make models total as $\text{mkTotal spec} = \lambda x.\text{[i] s}$, but that adds the do-nothing transition to each and every state. Here we want the do-nothing transition only if nothing else is defined.

In a similar way we can make a specification deterministic by selecting only the first state if there is more than one target state. In [11] we use this technique to transform specifications of thin client web-applications without browser navigation to specifications that cover browser navigation.

Apart from transforming specifications in order to give it desirable properties such as totality and determinism, it is
also possible to test whether a specification possesses these properties. We will use the possibilities of \texttt{G\textbackslash vst} to test logical properties. Testing logical properties is briefly revisited in section 5. In section 6 we use these techniques to verify properties of specifications.

5. Testing logical properties

Not all desirable properties of specifications can be guaranteed by a transformation of the specification. Nevertheless, we often want to know whether a specification possesses a property (e.g. like being deterministic). We will show that automated systematic testing can reveal whether a specification possesses a property and give examples for several important properties. In this section we review automated systematic testing introduced in [12].

Apart from state machine based testing, \texttt{G\textbackslash vst} is also able to test logical properties like $\forall x : \mathbb{N}.x + 1 > x$. In \texttt{G\textbackslash vst} such a property is represented by a function. The function arguments represent its universally quantified variables. For our example we use the type \texttt{Int} to represent \texttt{N}. This implies that the predicate has type \texttt{Int \rightarrow Bool}. The body of the function representing this property is just $p \ x = x + 1 > x$.

A universally quantified property is tested by evaluating the corresponding function for a large number of arguments. The function \texttt{test} in \texttt{G\textbackslash vst} initiates testing. A simplified, but correctly executing, version of this function is:

\begin{verbatim}
test :: p :~ Bool | holds p
  test p = and (take MaxTests (holds p))

  class holds a :: a :~ Bool
  instance holds b where holds b = [b]

  instance holds (a\rightarrow b) | gen [a\rightarrow b] a & holds b // Forall
  instance holds (For a b) | holds b // for a given test suite

  where holds (For p t) = diagonal (holds (p a) \\ a=gen [w])

  :: For a b = FOR infix 0 (a\rightarrow b) [a] // the infix for operator
  instance holds (For a b) | holds b // for a given test suite

  where holds (For p t) = diagonal (holds (p a) \\ a=gen [w])
\end{verbatim}

The text in the type signatures after \texttt{|} are context restrictions. Identifiers followed by \texttt{|[w]} refer to instances of generic functions, see appendix, for the given type. Identifiers without such suffix refer to ‘ordinary’ overloaded functions. An expression like [f a b | a \rightarrow 1, b \rightarrow m | p a b] is a list-comprehension, it computes the list of all values $f \ a \ b$ for all values $a$ coming from list $l$ and $b$ from list $m$ that satisfy the predicate $p \ a \ b$. There can be any positive number of generators (of the form $a \rightarrow 1$) and the predicate is optional.

The function \texttt{test} takes \texttt{MaxTests} elements from the list of booleans produced by \texttt{holds p}. The function and yields true if all these booleans are true and false otherwise. Here we need only three instances of the class \texttt{holds}. The instance for booleans just yields the list containing that boolean. The instance for a function corresponds to a universally quantified property. The list of test values is generated by \texttt{gen [w]}, see appendix. The final instance uses the given test suite instead of the one generated by \texttt{gen [w]} . The property is applied to each of these test values, $p \ a$. We apply \texttt{holds} on this result; either a boolean value, or another universally quantified argument. The function diagonal takes a fair mix of values from multiple generators for properties with more than one generator, like $\forall x.\forall y.x + y = y + x$.

By default the number of tests, \texttt{MaxTest}, to be done is $1000$. The actual test algorithm gives more information than just true or false (\texttt{Fail}). The result is \texttt{Proof} if the property holds for all elements in the generated test suite (proof by exhaustive testing). The value \texttt{Pass} indicates that the property holds for the first \texttt{MaxTest} values, there are untested values in the test suite. If the result is \texttt{Fail}, \texttt{G\textbackslash vst} also indicates the number of tests done and the test values that cause the counterexample found. Apart from these universal quantified properties \texttt{G\textbackslash vst} contains a library with all operators from first order logic. As example, to test the property $p$ for all integers from 1 to 100 we execute:

\begin{verbatim}
Start = test (p For [1..100])
\end{verbatim}

The result is \texttt{Proof: success for all arguments after 100 tests}. When we want to test the property for an unbounded number of odd values we evaluate \texttt{test (p For [1..3..])}. The result is \texttt{Proof: success for all arguments after 1000 tests}. If we have no strong opinion about the test values to be used we can leave it to the generic algorithm of \texttt{G\textbackslash vst} to generate the test values by executing \texttt{Start = test p}. Now the result is \texttt{Counterexample 1 found after 5 tests: 2147483647}. The property $p$ holds in mathematics, but not for the finite precision approximation of integers used in computers. The counterexample found is maxint in 32-bit integers. Because values such as 0, 1 and maxint often cause problems in properties, these test values are generated quickly by \texttt{G\textbackslash vst}.

6. Testing the quality of specifications

In this section we show how automated systematic testing as introduced in the previous section can be applied to models of state machines to determine whether relevant properties known from the literature holds for these specifications. This analysis of specifications can reveal errors in the specification, or increase the confidence in its correctness and consistency.

In the next subsection we define the generation of states and inputs used in the tests. Since an ESM used as specification can have an infinite number of states, it is convenient to define equivalence classes. The behavior of the specification in only one state in such an equivalency class needs to be tested. In subsequent subsections testing of a number of well known properties is discussed.
6.1. State space equivalence classes

For many specifications the state space or the number of possible input values is unbounded. Tailor made definitions of the generation of instances for these types makes the tests more effective and efficient. For instance we can consider only the states with multiples of 5 cents for the vending machine and obtain a sensible maximum amount of 200 cents in the machine by:

\[
\text{gen} \{\text{State} \} \ n \ r = \{ s \ i \ \land \ i \in \{0.5..200\}\}
\]

Similarly, in the tests of the Qui-Donc specification we use only states with digits that are on track to one of the few known numbers, the emergency number, or the invalid number that contains ten zeros. This is by no means a restriction of \(G \forall s t\), it is used as an illustration. The inputs for the vending machines can just be derived by \text{gen}. For the inputs of Qui-Donc we make sure that only the numbers \(0..9\) are used as argument for \(\text{Digit}\).

A key idea to test many properties effectively is to define equivalence of states and inputs for the tests. In the tests we try to take at least one representative of each (important) equivalence class. This approach is inspired by abstract interpretation in proof systems [4] and collapsing of states in model checkers like Spin [6] and Uppaal [2].

\[
\text{class \ equiv \ a :: a \rightarrow Bool}
\]

For example, in vending machines all states that represent more than 200 cents of inserted money are considered to be equivalent. These states represent the situation that relatively much money has been inserted.

\[
\text{instance \ equiv \ State}
\]

\[
\text{where \ equiv \ (S n) (S m) = n > 200 \&\& m > 200 \land n = m}
\]

We will use this equivalency of states in Sect. 6.5 and Sect. 6.7. In the rest of this section we discuss a number of properties of state machines known from literature and how these properties can be tested by \(G \forall s t\).

6.2. Testing whether a specification is total

As defined above, a specification is total if a transition is defined for each combination of input and output. We can easily test this by requiring that the number of transitions should not be empty:

\[
\text{isTotal :: (Spec s i o) s i \rightarrow Bool \ | \ gen \{i\*\} s \&\& gen \{i\*\} i}
\]

\[
\text{isTotal spec s i = \neg(\text{isEmpty (spec s i))}}
\]

We test the coffee vending machine for this property by evaluating test \((\text{isTotal specC})\). The test result is Counterexample 1 found after 4 tests: \((S 0) \text{ButT.}\) When we make the specification complete by \(\text{mkTotal}\) as defined above we test \(\text{isTotal (mkTotal specC)}\). Testing yields \(\text{Proof: success for all arguments after 164 tests.}\) Since we use \(\text{mkTotal this is hardly a surprise, it is more a check of the specification transformer \text{mkTotal.}\) Testing whether the combined vending machine is total by evaluating \(\text{test (isTotal specV)}\) gives an identical proof result, which is less evident. The proofs are only possible due to the finite number of states generated. When generation of states would have been derived the result of the last test is \(\text{Pass}\).

6.3. Testing whether a specification is deterministic

A specification is deterministic if there is at most one output and target state defined for each combination of state and input. By design \(G \forall s t\) can handle nondeterministic specifications. Even deterministic systems can be specified by nondeterministic specifications due to lack of information in the specification. The Qui-Donc system is supposed to be deterministic, but its specification is nondeterministic because we do not model the database containing the number information. This implies that the specification has to allow the situation that the number is known as well that it is unknown to the system.

Nevertheless, it can be important to know whether a specification is deterministic or not. It is tempting to test this by requiring that the length of the yielded list of transitions, \([\text{Trans} \circ s]\), is at most one. However that is too simplistic for two reasons. First, a yielded transition can be a function, \(\text{Ft f}\). Such a function \(f\) is supposed to accept any output as argument. Hence it cannot be deterministic. The second problem by testing the length of the list of resulting transitions is that it can contain the same pair of output and target state \((\text{pt} \circ o)\) twice, for instance by composing specifications as in \(\text{specC} \&\& \text{specT}\). So, a good test for checking whether a specification is deterministic verifies that all pairs in the list of transitions are identical and that this list does not contain functions:

\[
\text{isDeterministic :: (Spec s i o) s i \rightarrow Bool \ | \ gEq\{i\*\} o \&\& gEq\{i\*\} i}
\]

\[
\text{isDeterministic spec s i = unique (spec s i) Nothing}
\]

\[
\text{where}
\]

\[
\text{unique [] \ pair = True}
\]

\[
\text{unique [p:] \ pair = False}
\]

\[
\text{unique [p:q]} \quad \text{Nothing = unique r (Just p)}
\]

\[
\text{unique [p:q:r]} \quad \text{(Just q) = p \&\& q \&\& unique r (Just p)}
\]

Using this property \(G \forall s t\) proves that \(\text{specC, specC2, specC3, specC4, specC5}\) and \(\text{specC} \&\& \text{specT}\) are deterministic. Even testing the combination of \(\text{specC2}\) and \(\text{specT}\) for determinism yields proof, although the specification contains the transition for a nickel twice. The Qui-Donc specification is not deterministic, the first counterexample found by \(G \forall s t\) is Counterexample 1 found after 182 tests: \(\text{Info Nothing 1} (\text{Digit 1})\).

6.4. Testing whether a specification is consistent

Transitions of the form \(\text{Ft f}\) deserve some special attention. The function \(f\) has type \([\text{o} \rightarrow \{\text{s}\}]\), that is given an output (list of element of type \(\text{o}\)) it yields the list of allowed target
states of type $s$. For a successful conformance test the list of
target states is supposed to be nonempty. It is tempting
to require that the list of target states of all functions of this
kind is nonempty, but that is too restrictive. It should be al-
lowed to have a specification of the form $\text{spec } s \rightarrow \text{spec } t \rightarrow g$, 
where the functions $f$ and $g$ together cover all possible out-
puts. The results of $f$ and $g$ for individual arguments can be
empty. Moreover, returning an empty list is a convenient
way to indicate a nonconformance of the suit.

So, making it impossible to return an empty list is too re-
strictive, but it is interesting to know if a given specification
yields an empty list of target states for some state, input and
output. This can be tested by the property $\text{isConsistent}.

\begin{verbatim}
isisConsistent :: (Spec s i o) [] s i → Property | TestArg o
isConsistent spec outputs s i
= case ft
    | ft t o spec s i of
    [] = prop True
    fs = defined (foldl (+) fs) For outputs
    where defined f s =¬(isEmpty f o)
\end{verbatim}

Testing the vending machines yields pass. One can argue
that the tests containing multiple products as output are not
very useful. We can test only single product output by
evaluating $\text{test } (\text{isConsistent specT } [[\text{Coffee}},[\text{Tea}]]). \text{Gvst proves}
this property in 164 tests. Since these specifications do not
contain functions, these test results are not very exciting. A
tea vending machine containing a function is:

\begin{verbatim}
specC2 :: State In → [Trans Out State] 1
specC2 (S n) Nickel = [Pt [] (S (n+5))] 2
specC2 (S n) ButT = [Pt \x o. if (n≥5 && o == [Tea]) [S (n-5)]] 3
specC2 s i = [] 4
\end{verbatim}

Testing this specification for consistency by evaluating $\text{test } (\text{isConsistent specC2})$ finds a counterexample for the arguments
$(S 20) \text{ Year[]}$ after 4 tests. This test result indicates that the
transition on line 3 yields an empty set of target states start-
ing from state $(S 20)$ on input $\text{ButT}$ and the empty output. The
specification can be corrected by replacing the empty list
([]) on line 3 by $[S n]$: for this state and input the state
should be unchanged on an empty output.

Testing the Qui-Donc specification for consistency yields
counterexamples for outputs that are not a single
string. We restrict the tested outputs to some known names
by letting $\text{Gvst evaluate test } (\text{isConsistent Qui specC2 })$
@Str "Hoogman*", 
@Str "Plasmeijer"],[Str "Achter"]). The result of this test is Proof
after 644 tests.

6.5. Testing the reachability of states

The reachability of specific states can reveal important
information about a specification. The vending machines
will not work as desired if they cannot reach a state where
an item can be the output. Similar Qui-Donc will not work
properly if the state $\text{info}$ cannot be reached. Reachability of
states is a typical property that is usually verified by model-
checkers.

The function $\text{reachable}$ yields the list of all states that are
reachable in $n$ steps according to specification $\text{spec}$ starting
from $\text{states}$. This function does a breadth first search of the
state space of the specification. Since many specifications
have a huge state space (e.g. above $10^{10}$ for Qui-Donc), we
provide some help to limit the number of states. The argu-
ment $\text{eq}$ defines equality on states to remove similar states
as introduced at the start of this section. The function $\text{input}$
defines the inputs to be used at the given state. Similarly,
the function $\text{out}$ generates the outputs for a transition of the
form $\text{ft } t$ in the given specification.

\begin{verbatim}
reachable :: Int (spec s i o) [] s s→cool (s→[i]) s i→[o])
reachable n spec states eq input out pred = states++ r n states []
where
r 0 states seen = []
r n states seen = new++ r (n-i) new (new++ seen)
where new = rmEquiv eq [t, s → states] i → input s
    , p → spec s i, t → targets p s i out
    | ¬(member eq seen t) && pred t
\end{verbatim}

We use auxiliary functions $\text{member} :: (x s→cool) [x] → \text{Bool}$ and
$\text{rmEquiv} :: (x s→cool) [x] → [x]$. Both functions are parameterized
by an equivalence relation of elements. The function $\text{member}$
checks if the given element occurs in the given list. The function
$\text{rmEquiv}$ removes elements from the given list that are
equivalent to an element previously occurring in that list.

For the Qui-Donc specification we have lists $\text{listQDin}$ of
all interesting inputs, and $\text{listQDstate}$ of the interesting states.
To determine reachability in Qui-Donc we try all inputs in
each state by using the anonymous function $\text{listQDin}$ as the
argument $\text{input}$ of $\text{reachable}$. We use only states that are
interesting by the predicate $\text{member equiv listQDstate}$. Using this
we can verify that the state $\text{emergency}$ (corresponding to the
number 112) is not reachable in 5 steps, but is reachable in
6 steps.

6.6. Testing whether a specification is ini-
tially connected

A state machine is initially connected if each state can be
reached from the initial state. Testing the reachability for all
interesting states can be done by the function $\text{reachable}$ from
the previous subsection. It is more informative to know
which states are unreachable than just the information that a
specification is initially connected or not. The unreachable
states from the given list of $\text{allstates}$ are delivered by:

\begin{verbatim}
unreached :: (Spec s i o) [] s s→cool (s→[i]) s i→[o])
unreached spec states eq input out pred allstates
= [x, x → allstates | ¬(member eq reachedStates x)]
where reachedStates = reachable 100000 spec states eq input out pred
\end{verbatim}

Testing initially connectedness is now just checking
whether the list of unreachable states is empty. All specifica-
tions in this paper appear to be initially connected for the
states considered. It is clear that $\text{spec}$ cannot reach states
like S 5 and S 15, since it accepts only dimes. This is found promptly by G\textit{vst}.

6.7. Testing whether a specification is strongly connected

A state machine is strongly connected if every state can be reached from every other state. This is a rather strong restriction on state machines that is sometimes unwanted. If we know already that a state machine is initially connected it is strongly connected if the initial state is reachable from every other state.

\[
\text{connected :: (Spec s i o) s (s→Bool) (s→[i]) (s→[i]=[])\} \] (s→Bool) [s] → Bool.
\]

\[
\text{connected spec initState eq inp out pred allstates} = \text{and [member eq (reachable10000 spec s) eq inp out pred] initState allstates]}
\]

All state machines treated in this paper appear to pass the test for strong connectedness if we restrict ourselves to interesting states that are equivalent.

7. Domain specific properties

The properties handled in the previous section are problem independent in the sense that we can determine for each state machine whether these properties hold. With some effort one can also determine problem specific properties and test whether these properties hold for the specification at hand. Testing the specification for such a property increases the confidence in its correctness. We illustrate this with some simple examples.

We require that our vending machines are fair: they should not lose money. This implies that the amount of money \( n \) in state \( \text{S n} \) is a fair representation of the difference between the amount of money inserted and the value of all products obtained. Instead of checking this property for many traces, we check it for all transitions. If it holds for all transitions and the initial state \( \text{S 0} \), it will hold forever by induction. First we define a class value and the appropriate instances. The key instances cover the input \( \text{In} \) and output \( \text{Out} \) of the specification.

```
instance value State where value (S n) = n
instance value In  where value i = case i of Nickel = 5; Dime = 10
instance value Out where value o = case o of Coffee = 10; Tea = 5
```

Some additional instances are needed, e.g. to compute the value of a list of elements. Now the key property is easily defined. For all inputs and outputs of a given specification the value of the input and the current state has to be equal to the value of each transition (i.e. output plus target state) that is defined by the specification. This is expressed by:

```
propFair :: (Spec State In Out) State In → Property
propFair spec s i = p For spec s i
where p t = value s + value i == value t
```

Testing with this property shows that \text{mkTotal} is not as innocent as it looks. If we test \text{mkTotal spec}, the first counterexample \((S 5)\) \text{Nickel (Pt \{S 5\})} is found after 5 tests: the user inserts money but the state remains unchanged. The specification \text{mkTotal spec} is unfair!

In the same way we define a property that states that the amount of money in all reachable target states is non-negative when we start with a non-negative amount of money.

```
propNoDebit :: (Spec State In Out) State In → Property
propNoDebit spec s i = n≥0 \implies ((\lambda t o (S n)).m≥0) For spec s i
```

For the states generated as described above, \text{G\textit{vst}} proves this property. However, if we leave the generation of states to \text{G\textit{vst}} by stating \text{derive gen State}, \text{G\textit{vst}} finds the counterexample \text{2147483647 dime (Pt \{S \}} \text{-2147483639)} after 6 tests. The problem is caused by integer overflow in the counter of the state.

8. Finding domain specific properties

Above we have shown that it is possible to find issues in a state based specification by testing logical properties of such a specification. Some of these properties are universal and can be used for (almost) any specification, these properties are typically looked up in textbooks. This leaves us with the problem of finding domain specific logical properties. In our experience an effective way to obtain such domain specific properties for specifications is by simulating or inspection of the specification. When we manually find an incorrect transition, we can often find a domain specific logical property of the specification by generalization of the behavior we require instead of the erroneous transition.

The \text{esmViz} tool [10] can simulate and visualize the models used by \text{G\textit{vst}} for MBT. Using this tool we can interactively generate an expanded state diagram of the specified extended state machine. In a diagram of an \text{extended} state machine all states that differ only in a parameter are mapped to the same node in the graph, e.g. the states \( \text{S n} \) in figure 1 and the states \( \text{Digits i n} \) in figure 2. In an expanded state diagram states that differ in only a parameter are drawn as separate nodes, e.g. the states \( \text{On 20} \) and \( \text{On 30} \) in figure 4.

We demonstrate this with the specification of a vending machine that should be able to produce tea for 10 cents, coffee for 20 cents and chocolate for 30 cents. The user can insert coins up to a value of 40 cents. A specification containing almost as many nasty errors as we could slip in is:

```
:: State = Off | On Int
:: Input = SwitchOn | SwitchOff | Coin Int | Hot Product
:: Product = Coffee | Tea | Chocolate
:: Output = Cup Product | Return Int
```

We demonstrate this with the specification of a vending machine that should be able to produce tea for 10 cents, coffee for 20 cents and chocolate for 30 cents. The user can insert coins up to a value of 40 cents. A specification containing almost as many nasty errors as we could slip in is:

```
:: State = Off | On Int
:: Input = SwitchOn | SwitchOff | Coin Int | Hot Product
:: Product = Coffee | Tea | Chocolate
:: Output = Cup Product | Return Int
```

```vspec :: State Input → [Trans Output State]
vspec Off SwitchOn = [Pt \{\} (On 0)]
vspec s SwitchOff = [Pt \{\} Off]
```
Figure 4. An expanded state diagram of \( v_{\text{spec}} \) as generated by esmViz.

An expanded state diagram of this machine is depicted in figure 4. The transitions shown in this diagram correspond to inputs chosen by the user of the tool esmViz. Hence, the expanded state transition diagrams are often incomplete. This has the advantage that they are concise and the user can focus better on specific behavior. Based on a specification like \( v_{\text{spec}} \) the tool presents the inputs allowed in the current states, and draws an expanded state diagram of the transitions chosen by the user. If we look carefully at the transitions in this diagram we see some issues.

8.0.1. Wrong product. In figure 4 the delivered product in transition On 20 \( \text{But Tea/Cup Coffee} \) On 0 is not the required product. This is caused by the variable \( \text{coffee} \) in the function alternative \( v_{\text{spec}} \) (on s) (But coffee). Here the constructor Coffee (starting with an upper case) was needed. As a general property we state that the delivered product should always be equal to the required product:

\[
\text{pProduct :: State Product \rightarrow Property} \\
\text{pProduct s p = checkProd For v_{\text{spec}} s (But p) where checkProd (Pt \{\text{Cup q} \ t\}) p = p \text{== q}} \\
\text{checkProd _ } = \text{True}
\]

Testing this reveals many issues containing Tea or Chocolate as required product and states with a value \( s \text{On n with } n \geq 20 \).

8.0.2. All products. Given this erroneous transition, we might wonder if the specified machine is able to produce tea. More general: we expect that for all products \( p \) in the type Product the machine is able to produce a cup of that product for the input but \( p \).

\[
\text{pAllProducts :: Product \rightarrow Property} \\
\text{pAllProducts p = \{exists n. (isEmpty [p \ \_ Pt \{\text{Cup q} \ t\}) \_ v_{\text{spec}} s (But p) p \text{== q}]\}}
\]

In this property we use the keyword \( \exists \) to indicate an existentially quantified property (\( \exists \)), rather than an universally quantified property (\( \forall \)). \( \text{G\text{"{v}}\text{\text{"{s}}}} \) produces the counter examples Tea and Coffee. The test suite is generated using the generic algorithm by \texttt{derive gen Product}, hence it is known to contain all possible products.

8.0.3. Losing money. Note that the machine loses the value of the input coin in the transition On 50 \( \text{Coin 10/} \).

On 50. As a general version of this issue we state that all transitions must be fair as introduced in section 7. Also for this property \( \text{G\text{"{v}}\text{\text{"{s}}}} \) finds 12 issues. They correspond to 1) the transition for \( v_{\text{spec}} \) (on s) (Coin c) where \( s \geq \text{Max} \) (the unfair transition observed), 2) the second class of issues is caused by switching the machine off in a state On s with \( s \text{== 0} \), 3) also the production of coffee instead of tea is caught by this property since the value of coffee, 20, is unequal to the amount of the state change (10).

8.0.4. Illegal states. From the presence of a state On 50 in this diagram we conclude that it is possible to reach a state with too much money in the machine. The general property states that for all reachable states, the value of the target state after a transition is less or equal to \( \text{Max} \). We can easily generate only allowed states by:

\[
\text{gen \{\text{state} \} = \{\text{Off} v \text{\_} v \leftarrow \{0.5..\text{Max}\}\}}
\]

Exactly the same approach is used to verify that the amount of money in the machine is never negative as demonstrated in the previous section.

8.1. An improved specification

An improved version of this specification reads:

\[
\text{v_{\text{spec}}2 :: State \rightarrow State} \\
\text{v_{\text{spec}}2 \text{Input \rightarrow Trans Output State}} \\
\text{v_{\text{spec}}2 \text{SwitchOn = Pt \{\text{Return (value s) Off}\} \text{Off}}} \\
\text{v_{\text{spec}}2 \text{(on s) (Coin c) \leftarrow \{s \leq \text{Max}\} \text{\_} \{\text{Return c} \text{\_} (on s)\}}} \\
\text{v_{\text{spec}}2 \text{Output s SwitchOff = Pt \{\text{Return c} \text{\_} (on s)\}}} \\
\text{v_{\text{spec}}2 \text{State input \rightarrow \{\}}}
\]

Within a split second \( \text{G\text{"{v}}\text{\text{"{s}}}} \) proves that all listed properties hold for this specification even if we enlarge \( \text{Max} \) to 4000.

We have successfully used the same approach for the Qui-Donc system. This system has more than \( 10^{10} \) states. The expanded state diagram becomes completely unreadable if we try to draw a significant fraction of these states. Fortunately it is not needed to draw large amounts of states. To our rescue we noted that all interesting traces contain about 20 transitions or less. For these experiments it appeared useful, but not essential, to use 5-digit internal phone numbers rather than the 10-digit numbers used in the full specification. The esmViz tool contains several operations to prune the obtained diagram in order to keep it small and clear.

9. Related work

It is widely recognized that the quality of specifications in software engineering is important and not self-evident.
Using MBT to determine the quality of models however is rare. Back in the 80’s there was some initial work [8, 9], but after that people seem to rely on inspection by humans (as part of the pretest quality assurance) or model checkers like Upaal [2]. Using a proof system requires a transformation of the G/vst-model to a format understood by the model checker. Usually the model must be simplified to enable the model checker to prove the specified properties. Our approach has as advantage that the same model can be used for MBT of a sut and as subject of testing properties. For finite cases G/vst is able to produce a proof by exhaustive testing. If the search space is too large for exhaustive testing, our test system can still increase the confidence by doing many useful tests.

An ESM used as specification can be depicted as UML state chart. This is fine to get an overview, but it is cumbersome to make such a state chart a complete specification. None of the UML tools offers an expressive power similar to Clean. Hence the state chart has to be restricted, or entered as free text. Moreover, specifications in UML cannot be changed and composed like the models in G/vst. ProB [14] is able to verify properties like consistency and refinement of specifications in the language B by automatic testing. In our approach it is much easier to add domain specific properties, and our specifications can directly be used in simulations and to test the sut.

The Quickcheck [3] tool is only able to test logical properties, not the state based systems used here. Also Alloy [7] handles logical properties.

10. Conclusions

MBT of state based systems often reveals issues in the system under test as well as in the model used as basis for testing. This is not very strange: the software and its specification are similar formal artifacts, and it is known that humans do make errors in creating them. High level languages and analysis, like static type systems, reduce the number of errors, but do not eliminate them completely. If the tested system and the model do not contain the same error an issue is found during the test. This implies that not all errors in the specification pass unnoticed. But, late detection of errors in the specification can delay the software process. Even if the errors in a specification are found, it hampers progress. So, it is worthwhile to spend additional effort in improving and verifying the quality of specifications. As a rule of thumb test managers say that 40% of the issues found in automatic testing (executing scripts) correspond to errors in the sut. In our Model-Based testing experience with G/vst this is on average 75% which is already an important improvement. Using the techniques introduced here we are able to improve our models significantly. This should increase the fraction of issues that indicate errors in the sut, but we have not yet enough experience to give figures.

In this paper we have shown how we can give specification properties like being total by specification transformations. More important, we show how the logical branch of the test system G/vst can be used to express desirable properties of specifications for state based systems. Using some examples we demonstrated that issues in such a model can indeed be found by testing. Manual verification is still needed to detect domain specific issues. If we generalize these issues to constraints that should hold, G/vst is able to spot similar issues quickly and accurately.

Our approach heavily builds on modeling specifications as functions in a functional programming language. The advantages of this approach are clear semantics, concise specifications, the language compiler checks many aspects of the specifications. In this paper we have shown that it is possible to check additional properties of specifications, domain specific as well as more general properties, within the same framework. There is empirical evidence that people without any back ground in functional programming are able to write design required properties, implement them in the test tool and find issues within two weeks.

Appendix: Generic programming

Generic programming [5, 1] enables us to write algorithms once and then use these algorithms for any type. This technique builds on a uniform representation of types within the language and compiler generated transformation to and from that representation. The minimal set of types needed for the generic representation of any type is:

\[
\begin{align*}
&\text{:: } \text{UNIT } = \text{UNIT} & \text{// any constructor} \\
&\text{:: } \text{PAIR } x \ y = \text{PAIR } x \ y & \text{// glue types } x \text{ and } y \text{ together} \\
&\text{:: } \text{EITHER } x \ y = \text{LEFT } x \ | \ \text{RIGHT } y & \text{// a choice between types } x \text{ and } y
\end{align*}
\]

The real implementation of generics in Clean uses some additional constructs to represent information about types and constructors. These are not essential to understand generic programming. As example we consider a user defined polymorphic data type \text{List} defined as:

\[
\begin{align*}
&\text{:: } \text{List } x = \text{Nil } | \ \text{Cons } (\text{List } x)
\end{align*}
\]

The generic system generates a consistent representation of the constructors \text{Nil} and \text{Cons}. Using the generic types defined above. The generic list, \text{Listg}, is just the choice between the generic version of the constructors (\text{Nilg} and \text{Consg}).

\[
\begin{align*}
&\text{Nilg } = \text{LEFT } \text{UNIT} & \text{// generic version of Nil} \\
&\text{Consg } a \ x = \text{RIGHT } (\text{PAIR } a \ x) & \text{// generic version of Cons} \\
&\text{:: } \text{Listg } x = \text{EITHER } \text{UNIT } (\text{PAIR } a \ x) & \text{// type of generic list}
\end{align*}
\]

The ubiquitous generic programming example is equality. If we define equality for the generic types in the obvious way, Clean can derive equality for lists by first transforming the lists to their generic representation and then compare these generic representations.
The functions and are generated and provided by the generic system to compare the arguments of the types and .

A more interesting example is the generation of lists of booleans. These , , and are generated and provided by the generic system.

For basic types like and , we use a boolean to indicate the turn. After these preparations the list containing only this constructor. For a we combine these lists in a breadth-first way. The generic system provides these lists as the arguments of the type . We start out by defining the generic type and instances for the basic generic types defined above.

The only possible element of type is the constructor . Hence the generic generator generates a singleton list containing only this constructor. For we need to combine elements from two lists. The generic system provides these lists as the arguments and . Using the library function we combine these lists in a breadth-first way. For the type we take elements from both provided lists in turn. We use a boolean to indicate the turn. After these definitions we can generate the derivation of elements of type .

For basic types like we have to specify the possible values.

After these preparations the Clean system is able to generate Lists of Booleans. These values are used by GVS to test properties over these lists. The initial fragment of this list is: [Nil, Cons False Nil, Cons True Nil, Cons False (Cons False Nil)],....

References


